



DATA DISPLAY GROUP

Datasheet

Samsung

LTI700HA02-0

SA-01-276

The information contained in this document has been carefully researched and is, to the best of our knowledge, accurate. However, we assume no liability for any product failures or damages, immediate or consequential, resulting from the use of the information provided herein. Our products are not intended for use in systems in which failures of product could result in personal injury. All trademarks mentioned herein are property of their respective owners. All specifications are subject to change without notice.



SAMSUNG DISPLAY



Product Specification

- () Product Information
- () Preliminary Specification
- () Approval Specification

Any modification of Spec is not allowed without SDC's permission.

CUSTOMER	G/A Customer	MODEL NO.	LTI700HA02
DATE OF ISSUE	2013/07/23	EXTENSION CODE	-0

Customer Approval & Feedback

Approved by	<i>Kang Sang Rae</i> 2013/07/23
Prepared by	<i>Kyungil Oh</i> 2013/07/23

LCD Sales & Marketing Center Samsung Display Co., Ltd
--

Table of Contents

REVISION HISTORY	3
GENERAL DESCRIPTION	4
1. ABSOLUTE MAXIMUM RATINGS	
1.1 ENVIRONMENTAL ABSOLUTE RATINGS	6
1.2 ELECTRICAL ABSOLUTE RATINGS	7
1.3 THE OTHERS ABSOLUTE RATINGS	7
2. APPLICATION INFORMATION FOR PID(PUBLIC INFORMATION DISPLAY)	8
3. OPTICAL CHARACTERISTICS	9
4. ELECTRICAL CHARACTERISTICS	
4.1 TFT LCD MODULE	12
4.2 BACKLIGHT UNIT	13
4.3 LED CONVERTER CHARACTERISTICS	14
5. INPUT TERMINAL PIN ASSIGNMENT	
5.1 INPUT SIGNAL & POWER	15
5.2 LED CONVERTER INPUT PIN CONFIGURATION	17
5.3 LED CONVERTER POWER SEQUENCE	17
5.4 LVDS INTERFACE	18
5.5 INPUT SIGNALS, BASIC DISPLAY COLORS AND GRAY SCALE	19
6. INTERFACE TIMING	
6.1 TIMING PARAMETERS (DE ONLY MODE)	20
6.2 TIMING DIAGRAMS OF INTERFACE SIGNAL (DE ONLY MODE)	21
6.3 CHARACTERISTICS OF INPUT DATA OF LVDS	22
6.4 THE SEQUENCE OF POWER ON AND OFF	24
7. OUTLINE DIMENSION	24
8. RELIABILITY TEST	27
9. PACKING	28
10. MARKINGS & OTHERS	29
11. GENERAL PRECAUTIONS	

REVISION HISTORY

Date.	Rev.No.	Page	Revision Description
2012/09/28	000	all	Approval Spec Firstly issued
2012/11/02	001	26	Reliability Items updated
2012/12/14	002	4	Add sentence 'Landscape only' and its symbol
		7	Static electricity pressure resistance - Contact Discharge : 8kV->10kV Air Discharge : 15kV->20kV
		8	2. Application information for DID a. The proper operating time: Under 20->12 hours a day.
		12	Current of power supply (MAX) : Apply the margin of consumption - Black 762->845, White 897-> 992, H-Stripe 1765->1950
		24, 25	7. Outline dimension - Import Real size drawing
2012/12/14	003	12	Current of power supply (Max Spec) Black : 762 → 845 White : 987 → 992 H-Stripe : 1760 → 1950
2013/03/07	004	16	Converter's slave pin map added (Master / Slave) The former spec missed the slave pin map. Master : 22022WR-H14B2 (YEONHO) CN2 Slave : 22022WR-H14B2 (YEONHO) CN1
2013/07/23	005	all	Change the Word "DID(Digital Information Display)" → "PID(Public Information Display)"
		8	Add the 2.5(About dust condition)

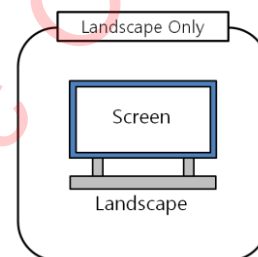
GENERAL DESCRIPTION

DESCRIPTION

LTI700HA02-0 is a color active matrix liquid crystal display(LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit, and a back-light unit. This 70.0" model has a resolution of 1,920 x 1,080 pixels (16:9) can display up to 16.7 Million colors with the wide viewing angle of 89° or higher in all directions.

FEATURES

- RoHS compliance(Pb-free)
- FHD(1,920X1,080) resolution(16:9)
- SPVA(Super Patterned Vertical Align) mode
- High Tni(85°C) Liquid Crystal
- High speed response
- High contrast ratio, High aperture ratio with the wide color gamut
- Wide viewing angle(±178°)
- Landscape only compatible
- LVDS(Low Voltage Differential Signaling) Interface(2pixel/clock)
- DE(Data Enable) mode
- Bar Type Edge LED(Light Emitting Diode)
- Low power consumption



APPLICATIONS

- Public Information Display(PID)
- High Definition Public Monitor

GENERAL INFORMATION

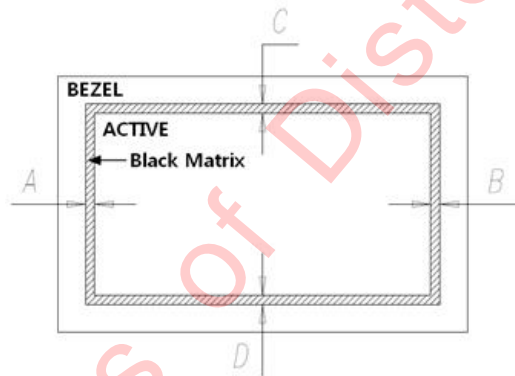
Item	Specification	Unit	Note
Display area	1549.44 (H) x 871.56 (V)	mm	
Driver Element	a-Si TFT active matrix		
Display colors	16.7M (8bit)		
Number of pixel	1,920 x 1,080	Pixel	
Pixel Arrangement	RGB Vertical stripe		
Display Mode	Normally Black		
Surface treatment	Haze 2.3% / 2H		Anti-Glare
Luminance of White	400(Typ)	cd/m ²	

MECHANICAL INFORMATION

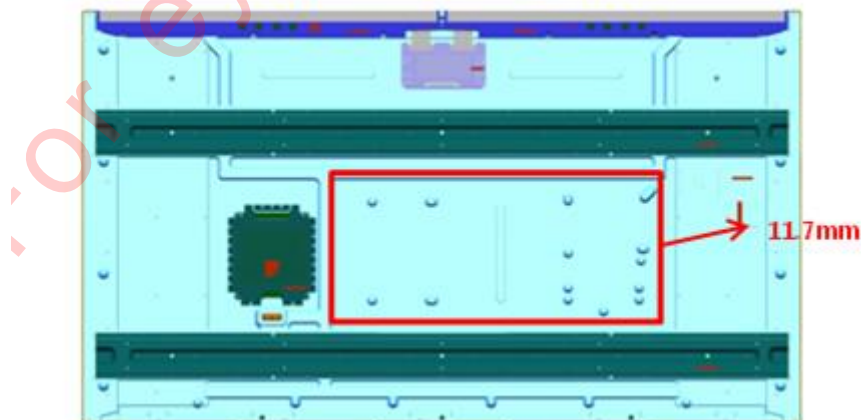
Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	1598.0	1600.0	1602.0	mm	
	Vertical (V)	920.0	922.0	924.0	mm	
	Depth (D)	10.7	11.7	12.7	mm	Minimum Depth (2)
Bezel Open	Horizontal (H)	1558.0	1560.0	1562.0	mm	
	Vertical (V)	880.0	882.0	884.0	mm	
Black Matrix Shift	Horizontal (H)	-	-	3.0	mm	(1)
	Vertical (V)	-	-	3.0	mm	
Weight		-	31,000	33,000	g	

Note (1) Measure the figure for **Black Matrix shift** to be recorded on the spec. with referring to the drawings.

- | A - B | ≤ Horizontal Spec
- | C - D | ≤ Vertical Spec



Note (2) Measure point of Depth



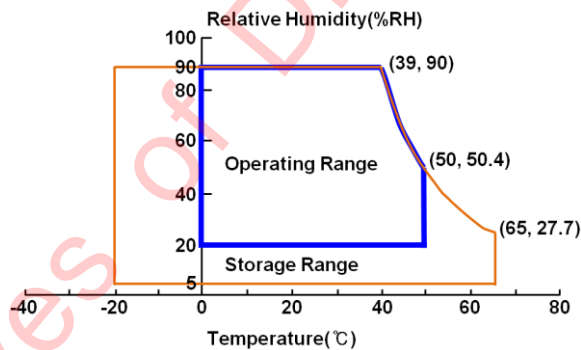
1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

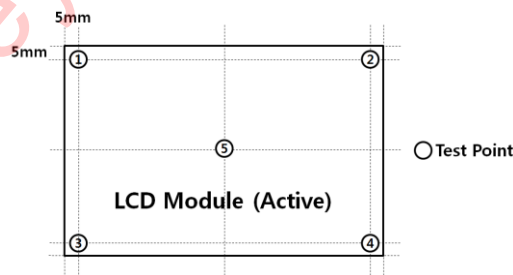
Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T _{STG}	-20	65	°C	(1)
Operation Temperature	T _{OPR}	0	50	°C	(1)
Glass Surface temperature (Operation)	Center	0	50	°C	(1), (2)
	T.Uniformity	-	10		
Storage humidity	H _{STG}	5	90	%RH	-
Operating humidity	H _{OPR}	20	90	%RG	-
Shock(non-operating)	Snop(X,Y,Z)	-	-	G	(3)
Vibration(non-operating)	V _{nop}	-	-	G	

Note(1) Temperature and relative humidity range are shown in the figure below.

- a. 90% RH Max(Ta ≤ 39°C)
- b. Relative Humidity is 90% or less(Ta > 39°C)
- c. No condensation



Note(2) Definition of test point



$$\Delta T \text{ should be less than } 10^\circ\text{C} (\Delta T = |T_{\text{CENTER}} - T_{\text{CORNER}}|)$$

T_{CENTER} : Temperature of the center of the glass surface (Test point 5)

T_{CORNER} : Temperature of each edge of the glass surface (Test point 1~4)

Note(3) Vibration and shock tests are not guaranteed due to limitation of equipment for this model.

1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VDD	10.8	13.2	V	(1)
Dimming Control	V _{Dim}	0	5.25	V	-

(2) BACKLIGHT UNIT(LED Unit)

Item	Symbol	Min	Max	Unit.	Note
Input Supply Voltage	V _{CC}	-	26.4	V	(1)

Note(1) Ta= 25 ± 2 °C

The permanent damage or defect to the device may occur if the panel is operated at the figure set, which exceeds a ceiling of maximum value stated in the former spec. The functional operation should be limited to the conditions described above under normal operating conditions.

1.3 THE OTHERS ABSOLUTE RATINGS

Static Electricity Pressure Resistance

Item	Test Conditions	Remark
Contact Discharge	150pF, 330Ω, ±10kV, 200points, 1time/point	Operating
Air Discharge	150pF, 330Ω, ±20kV, 200points, 1time/point	

2. APPLICATION INFORMATION FOR PID (Public Information Display)

A PID's screen may display the sudden image such as an image retention.
To extend the lifetime and optimize a function of module, the below-mentioned operating conditions are required.

2.1 Normal operating condition

- a. Temperature: 20 ±15℃
- b. Humidity: 55 ±20 %
- c. Display pattern: Moving image or image, which switches regularly.
Note) The sudden image on the screen can be displayed after the static image is shown in the long-term.

2.2 The operating conditions when the module is operated under the abnormal condition.

- a. Ambient condition
-It is recommended to set the PID up in the well-ventilated place.
- b. The function of power off and screen saver
-The function of periodical power-off or a screen saver is needed when the static image is displayed in the long-term.

2.3 Operating conditions to prevent the sudden display resulted from displaying the static image in the long-term.

- a. The proper operating time: Under 12 hours a day.**
- b. The moving image shall be inserted between the static displays periodically.
-The refresh time for liquid crystal is needed.
- c. The periodic changing of background color and character's color(image)
-Use the different color for background and character(image) respectively.
-Change colors periodically.
- d. Avoid combining the color for background with the color for character, which has a largely different luminance.
Note (1) Abnormal condition means all operating condition except normal operating condition.
Note (2) The moving image or black pattern is strongly recommended as a screen saver.

2.4 Only the lifetime of PID stated in this spec is guaranteed if the PID is used under the proper operating conditions.

2.5 Clean the system regularly for not accumulating the dust around the system considering user environment, otherwise, its reliability and function may not be satisfied.

3. OPTICAL CHARACTERISTICS

The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, SR-3, ELDIM EZ-Contrast

Ta = 25 ± 2 °C, VDD = 3.3V, fv = 60Hz, fDCLK = 148.5MHz, IF = 100% duty

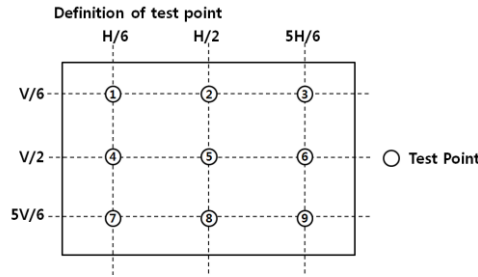
Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio	C/R	-	3000	4000	-	-	(3) SR-3	
Response time	G-to-G (AVG)	Tg	-	8	15	msec	(5) RD-80S	
Luminance of White (At the center of screen)	YL	-	300	400	-	cd/m ²	(6) SR-3	
Color Chromaticity (CIE 1931)	Red	Rx	Normal φ = 0 θ = 0 Viewing Angle	TYP. -0.03	0.640	TYP +0.03	-	(7), (8) SR-3
		Ry			0.333			
	Green	Gx			0.320			
		Gy			0.605			
	Blue	Bx			0.150			
		By			0.055			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut	-	-	66	70	-	%	(7) SR-3	
Color temperature	-	-	-	10000	-	K	(8) SR-3 EZ-Contrast	
Viewing Angle	Hor.	θL	CR ≥ 10	79	89	-	Degree	(8) SR-3 EZ-Contrast
		θR		79	89	-		
	Ver.	θU		79	89	-		
		θD		79	89	-		
Brightness Uniformity (9 Point)	Buni	-	-	-	25.0	%	(4) SR-3	

Note (1) Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the backlight at the given temperature for stabilization of the backlight. This should be measured in the center of screen.

LED : IF = 160mA (Per String)
Environment condition : Ta = 25 ± 2 °C

Note(2) Definition of test point



Note(3) Definition of Contrast ratio(C/R)

: Ratio of max.gray(Gmax) & min.gray(Gmin) at the center point ⑤ of the panel.

$$C / R = \frac{G \max}{G \min}$$

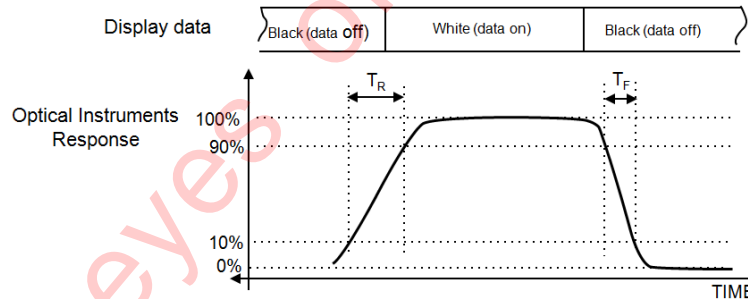
Gmax : Luminance in all white pixels
Gmin : Luminance in all black pixels.

Note(4) Definition of brightness uniformity at 9 points(Test pattern : Full white)

$$Buni = 100 * \frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note(5) Definition of Response time : Average response time of all Gray to Gray except Tr, Tf



※ G-to-G : Average response time between the whole gray scale to the whole gray scale.

Gray to Gray Response Time										
Gray	End									
	0	31	63	95	127	159	191	223	255	
Start	0	Tr(0-31)	Tr(0-63)	Tr(0-95)	Tr(0-127)	Tr(0-159)	Tr(0-191)	Tr(0-223)	Tr(0-255)	T _{ON}
	31	Tr(31-0)	Tr(31-63)	Tr(31-95)	Tr(31-127)	Tr(31-159)	Tr(31-191)	Tr(31-223)	Tr(31-255)	
	63	Tr(63-0)	Tr(63-31)	Tr(63-95)	Tr(63-127)	Tr(63-159)	Tr(63-191)	Tr(63-223)	Tr(63-255)	
	95	Tr(95-0)	Tr(95-31)	Tr(95-63)	Tr(95-127)	Tr(95-159)	Tr(95-191)	Tr(95-223)	Tr(95-255)	
	127	Tr(127-0)	Tr(127-31)	Tr(127-63)	Tr(127-95)	Tr(127-159)	Tr(127-191)	Tr(127-223)	Tr(127-255)	
	159	Tr(159-0)	Tr(159-31)	Tr(159-63)	Tr(159-95)	Tr(159-127)	Tr(159-191)	Tr(159-223)	Tr(159-255)	
	191	Tr(191-0)	Tr(191-31)	Tr(191-63)	Tr(191-95)	Tr(191-127)	Tr(191-159)	Tr(191-223)	Tr(191-255)	
	223	Tr(223-0)	Tr(223-31)	Tr(223-63)	Tr(223-95)	Tr(223-127)	Tr(223-159)	Tr(223-191)	Tr(223-255)	
	255	Tr(255-0)	Tr(255-31)	Tr(255-63)	Tr(255-95)	Tr(255-127)	Tr(255-159)	Tr(255-191)	Tr(255-223)	
T _{OFF}										

T*(X-Y) : Response time from level of gray at X to level of gray at Y

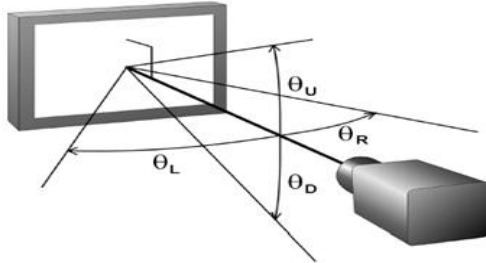
The definition of response time = $\sum [T^*(X-Y)] / 72$

Note(6) Definition of Luminance of White : Luminance of white at center point ⑤

Note(7) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note(8) Definition of Viewing Angle : Viewing angle range($C/R \geq 10$)



For eyes of Distec On

4. ELECTRICAL CHARACTERISTICS

4.1 TFT LCD MODULE

The connector to transmit a display data and a timing signal should be connected.

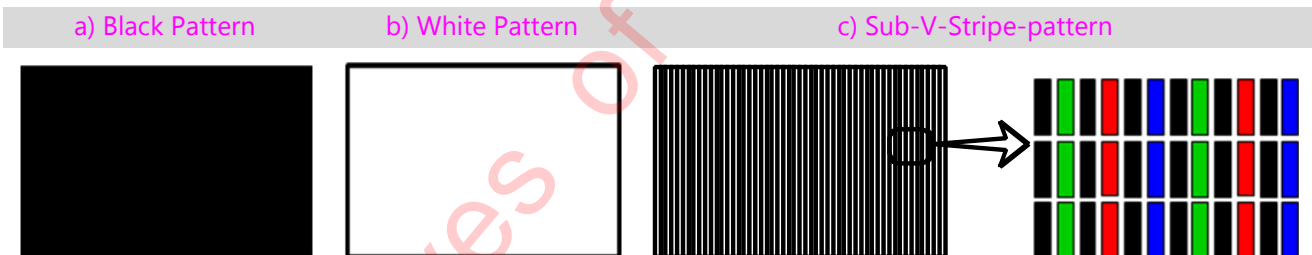
Ta = 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	V _{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	-	670	845	mA	(2), (3)
	(b) White	-	788	992		
	(C) N-Pattern	-	1520	1950		
Vsync Frequency	f _V	48	60	62	Hz	-
Hsync Frequency	f _H	54	67.5	69.75	kHz	-
Main Frequency	F _{dclk}	118.8	148.5	153.5	MHz	-
Rush Current	I _{RUSH}	-	-	7.0	A	(4)

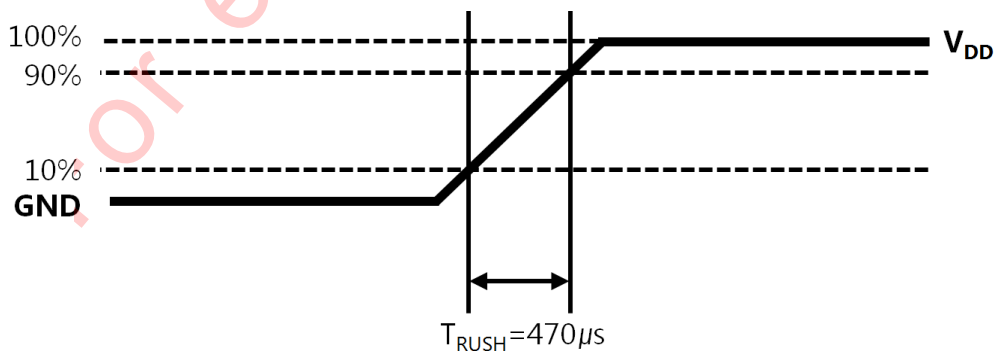
Note(1) The ripple voltage should be controlled under 10% of V_{DD}.

Note(2) f_V=60Hz, f_{dclk}=148.5MHz, V_{DD}=12.0V, DC Current.

Note(3) The pattern for checking the power dissipation(LCD module only).



Note(4) Conditions for measurement



The rush current, I_{RUSH} can be measured when T_{RUSH} is 470µs.

4.2 BACKLIGHT UNIT

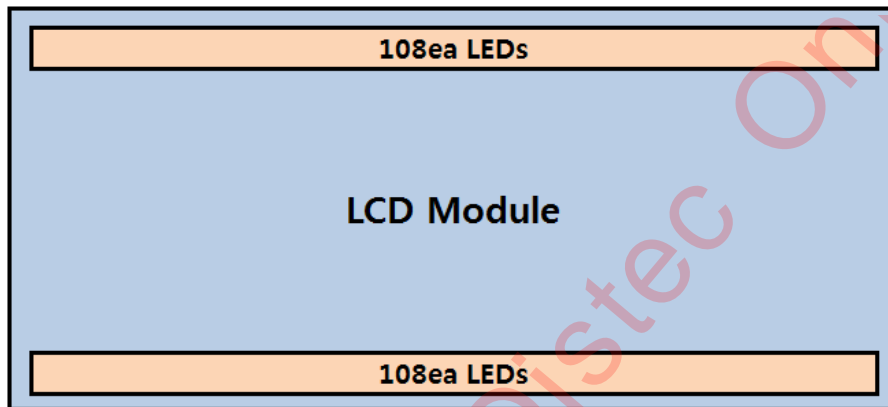
The back light unit contains Bar type 216 LEDs (Light Emitting Diode).
The characteristics of BLU are shown in the following tables.

Ta = 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	-	50,000	-	Hour	(1)

Note(1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = 25±2 °C, If = 160mA, For single LED string only]



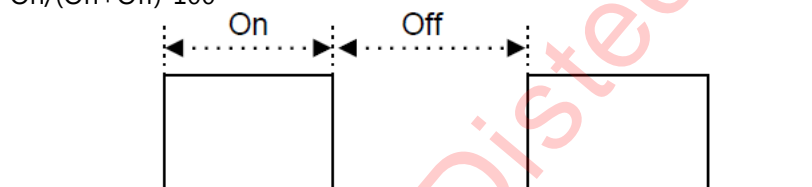
For eyes of Distec On

4.3 LED CONVERTER CHARACTERISTICS

Items	Symbol	Conditions	Specifications			Unit	Note
			Min	Typ	Max		
Input Voltage	V_{in}	-	22	24	26	V	$T_a=25\pm 2^{\circ}C$
Input Current (Inrush)	I_{in}	$V_{in} = 24V$ Dim = Max	-	-	13	A	-
Output Current	I_{LED}	$V_{in} = 24V$ Dim = Max	152	160	168	mA (rms)	Per String
Backlight On/Off Control	ENA	Enable	2.4	-	5.25	V	-
		Disable	0	-	0.8		
Analog Dimming	V_{A_Dim}	$V_{in} = 24V$	0	-	3.3	V	
	Duty		20	-	100	%	(2)

Note(1) All data was approved after running 120 minutes.

Note(2) High duty = On/(On+Off)*100



※ Additional appendix for supply current

Items	Symbol	Conditions	Specification			Unit
			Min	Typ	Max	
Input Current	$I_{in_Overshoot}$	$V_{in}=24V$, Dim=3.3V (Within 1hr at BLU On)	-	10.3	10.6	A
	$I_{in_Saturation}$	$V_{in}=24V$, Dim=3.3V (After 1hr Aging)	-	10.1	10.4	

5. INPUT TERMINAL PIN ASSIGNMENT

5.1 INPUT SIGNAL & POWER

Connector : FI-RE51S-HF-J (JAE)

PIN No.	Description		PIN No.	Description	
1	V _{DD} (12V)		26	Even LVDS Signal	Rx2[A]P
2	V _{DD} (12V)		27		Rx2[B]N
3	V _{DD} (12V)		28		Rx2[B]P
4	V _{DD} (12V)		29		Rx2[C]N
5	V _{DD} (12V)		30		Rx2[C]P
6	No Connection		31		GND
7	GND		32		Rx2CLK_N
8	GND		33		Rx2CLK_P
9	GND		34		GND
10	Odd LVDS Signal	Rx1[A]N	35		Rx2[D]N
11		Rx1[A]P	36	Rx2[D]P	
12		Rx1[B]N	37	No Connection	
13		Rx1[B]P	38	No Connection	
14		Rx1[C]N	39	GND	
15		Rx1[C]P	40	No Connection	
16		GND	41	No Connection	
17		Rx1CLK_N	42	No Connection	
18		Rx1CLK_P	43	No Connection	
19		GND	44	No Connection	
20		Rx1[D]N	45	LVDS_SEL	
21		Rx1[D]P	46	No Connection	
22		No Connection	47	No Connection	
23		No Connection	48	No Connection	
24	GND		49	No Connection	
25	Even LVDS	Rx2[A]N	50	No Connection	
			51	No Connection	

Note(1) No Connection : These pins are only used for SDC internal purpose.

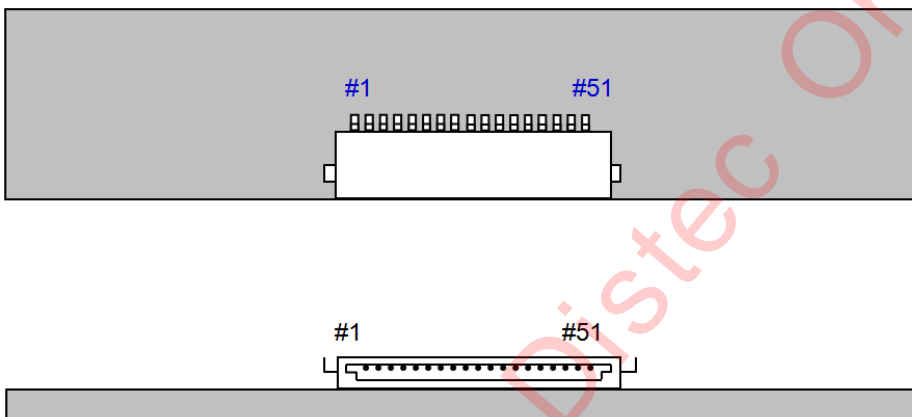
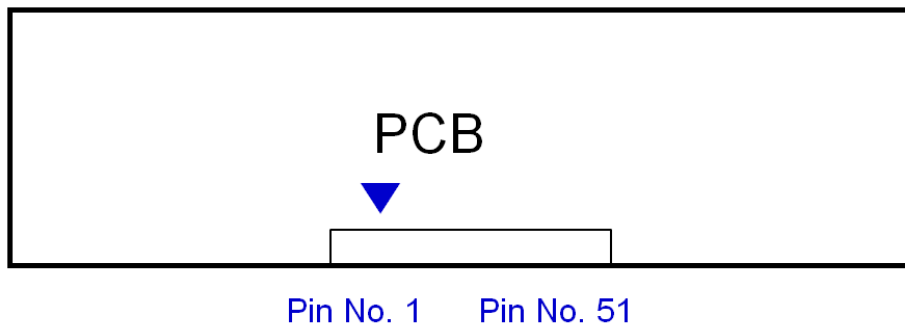
Note(2) LVDS Option : High(3.3V) → Normal NS LVDS format

Low(GND or N.C) → JEIDA LVDS format

Sequence : On = V_{DD}(T1) ≥ LVDS Option ≥ Interface Signal(T2)

Off = Interface Signal(T3) ≥ LVDS Option ≥ V_{DD}

Note (3) LVDS Connector



- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All N.C pins should be separated from other signal or power.

5.2 LED CONVERTER INPUT PIN CONFIGURATION

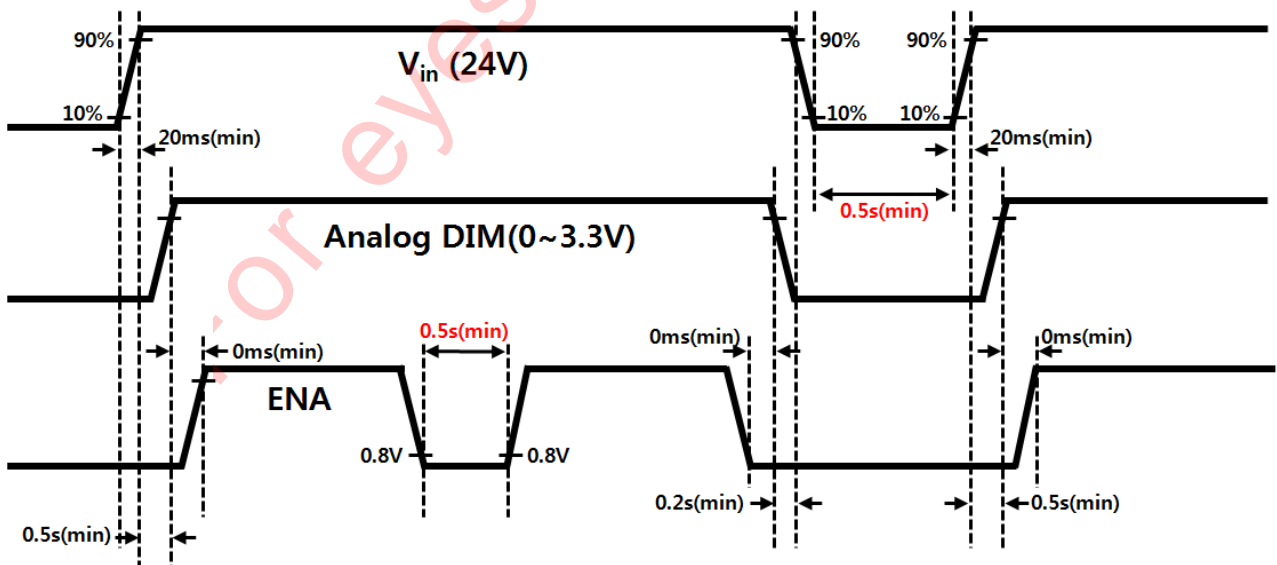
CN2 : Connector : YEONHO, 22022WR-014B2

PIN NO	Pin Configuration(FUNCTION)
1, 2, 3, 4, 5.	V _{in} (24V)
6, 7, 8, 9, 10	GND
11	No Connection
12	ENA (Converter On/Off Control Signal) DC 0~0.8V : Off / 2.4~5.25V : On
13	Analog Dimming Control [0V : Min / 3.3V : Max]
14	No Connection

CN1 : Connector : YEONHO, 22022WR-014B2

PIN NO	Pin Configuration(FUNCTION)
1, 2, 3, 4, 5.	V _{in} (24V)
6, 7, 8, 9, 10	GND
11	No Connection
12	No Connection
13	No Connection
14	No Connection

5.3 LED CONVERTER POWER SEQUENCE



5.4 LVDS INTERFACE

- LVDS Receiver : Tcon(Merged)
- Data Format(JEIDA & Normal)

Default LVDS Option : JEIDA

	LVDS pin	JEIDA -DATA	VESA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

LVDS Option : High(3.3V) → Normal NS LVDS format / Low(GND or N.C) → JEIDA LVDS format

5.5 INPUT SIGNALS, BASIC DISPLAY COLORS AND GRAY SCALE

COLOR	DISPLAY (8bit)	DATA SIGNAL																												GRAY SCALE LEVEL
		RED								GREEN								BLUE												
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7					
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	-	
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	-	
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	-	
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	-	
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0		
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1		
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R252		
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
		1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253		
	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254		
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255		
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0		
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1		
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G252		
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G253		
	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G254		
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G255		
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0		
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B252		
	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B253		
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B254		
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B255		

Note) Definition of Gray

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray(n = Gray Level)

Input Signal : 0 = Low Level Voltage, 1 = High Level Voltage

6. INTERFACE TIMING

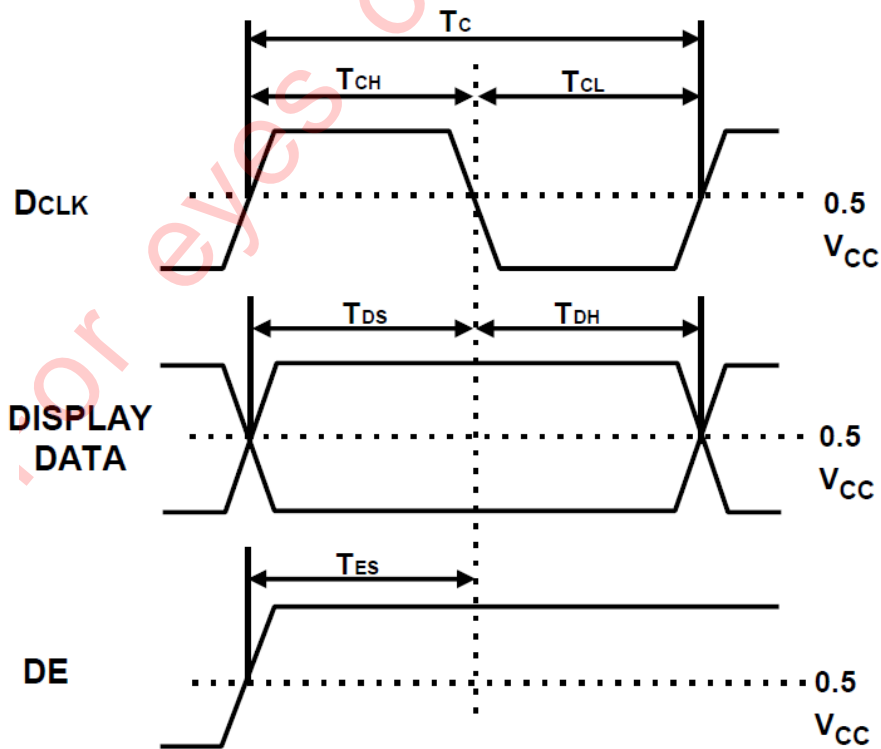
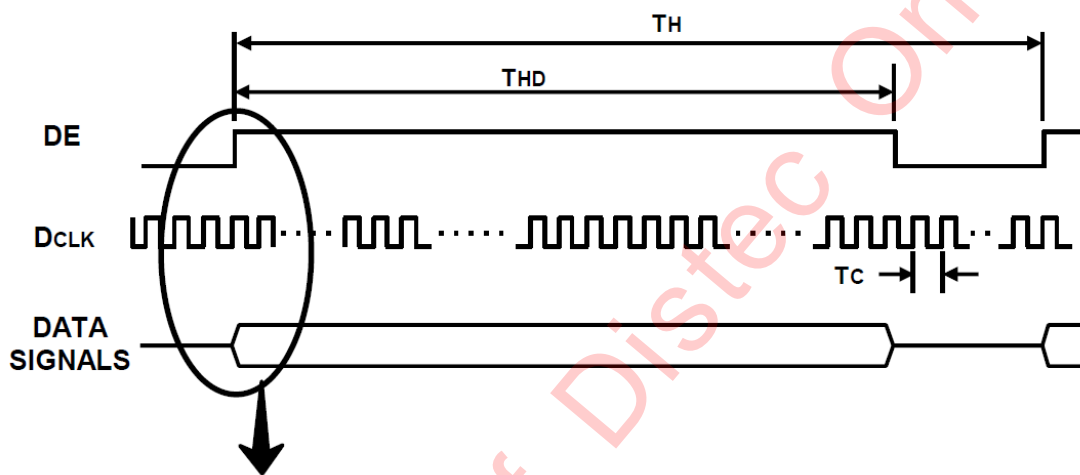
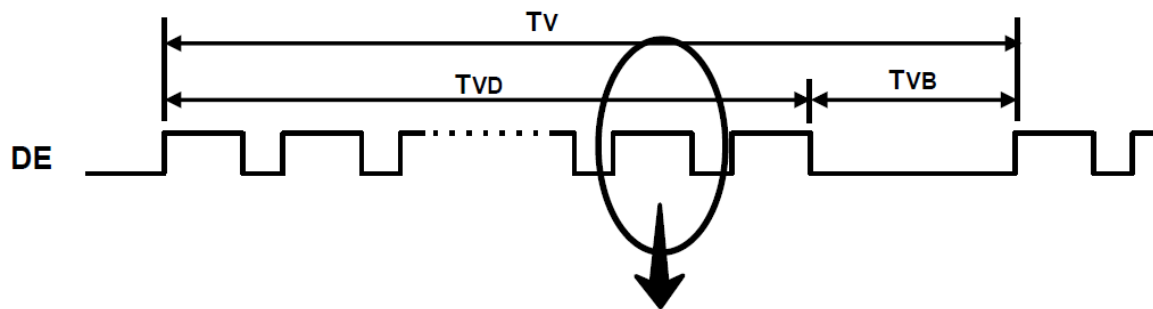
6.1 TIMING PARAMETERS (DE ONLY MODE)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	118.8	148.5	153.5	MHz	-
Hsync		F_H	54	67.5	69.75	KHz	-
Vsync		F_V	48	60	62	Hz	-
Term for the vertical display	Active display period	T_{VD}	-	1080	-	Lines	-
	Total vertical	T_V	1100	1125	1158	Lines	-
Term for the horizontal display	Active display period	T_{HD}	-	1920	-	Clocks	-
	Total Horizontal	T_H	2090	2200	2350	clocks	-

Note) The signals of Hsync and Vsync must be inputted even though this T-con is operated at DE mode.

- (1) Test Point: TTL controls signal and CLK at LVDS Tx at the input terminal of system.
- (2) Internal VDD = 3.3V
- (3) The spread spectrum
 - The limit of spread spectrum's range of SET in which the LCD module is assembled should be within $\pm 3\%$
 - Frequency for modulation : Min 30KHz ~ Max 300KHz

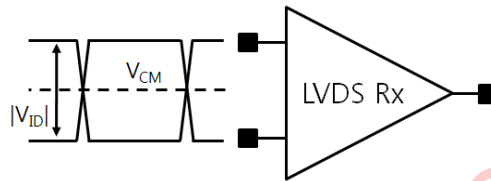
6.2 TIMING DIAGRAMS OF INTERFACE SIGNAL (DE ONLY MODE)



6.3 CHARACTERISTICS OF INPUT DATA OF LVDS

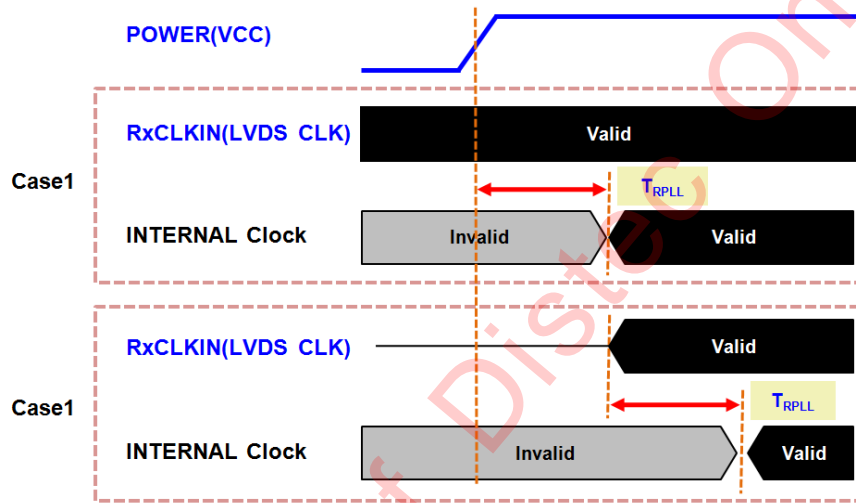
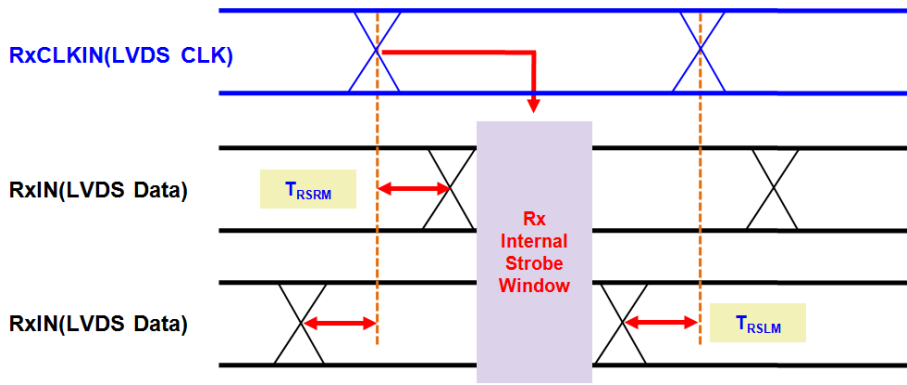
(1) Specification for DC

ITEM	SYMBOL	Min.	Typ.	Max.	UNIT
Supply voltage for IO	VDD33_LVDS	3.0	3.3	3.6	V
Supply voltage in the core	VDD12_LVDS	1.1	1.2	1.3	V
Color depth			8/10		Bit
Input voltage at the common mode	V_{CM}	0.3		1.8	V
Input voltage for differential	$ V_{ID} $	100	350	600	mV

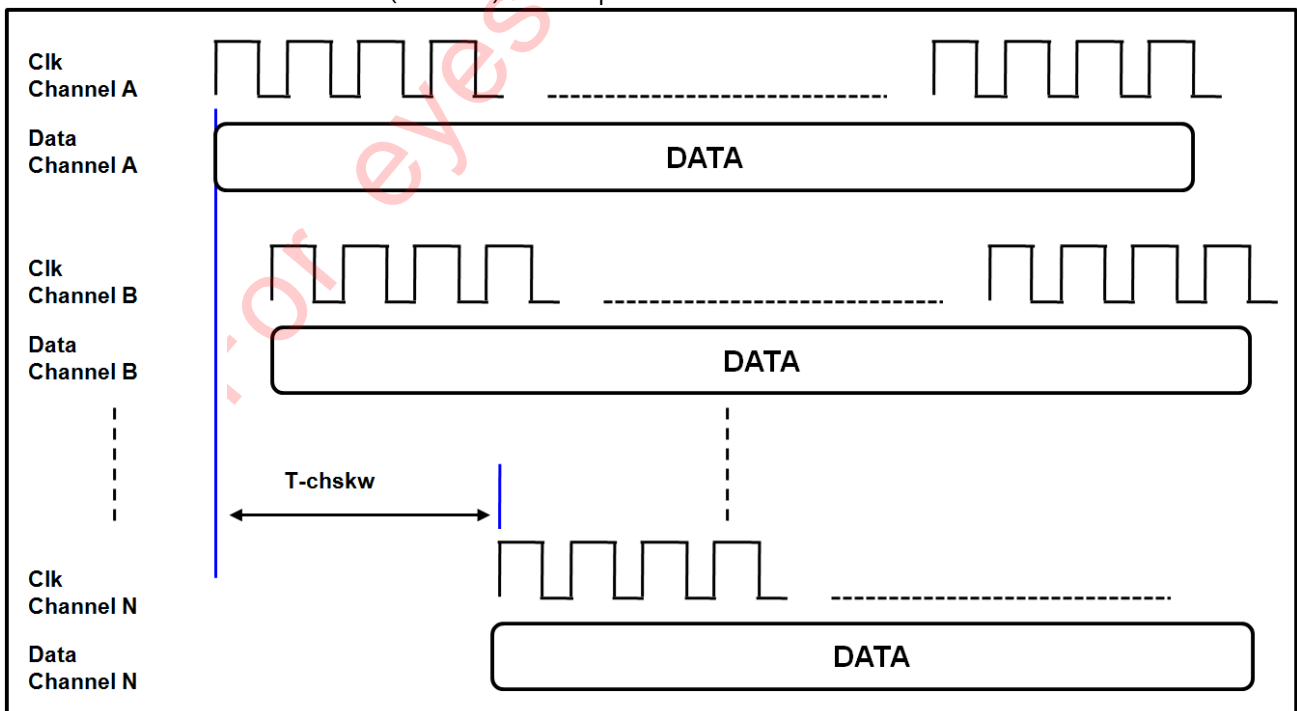


(2) Specification for AC

ITEM	SYMBOL	Min.	Typ.	Max.	UNIT	
Frequency for input clock (=1/T)	F_{IN}	25	-	90	MHz	
Period of output clock	t_{RCP}	11.11	-	40	ns	
Position of input data	$F_{IN}=85\text{MHz}$	t_{RSRM}	-	-	+400	ps
	$F_{IN}=78\text{MHz}$		-	-	+450	
	$F_{IN}=75\text{MHz}$		-	-	+500	
Position of input data	$F_{IN}=85\text{MHz}$	t_{RSLM}	-400	-	-	ps
	$F_{IN}=78\text{MHz}$		-450	-	-	
	$F_{IN}=75\text{MHz}$		-500	-	-	
Lock time	t_{RPLL}	-	-	100	usec	
Duty ratio of Rx's clock for output	T_{duty}	45	50	55	%	



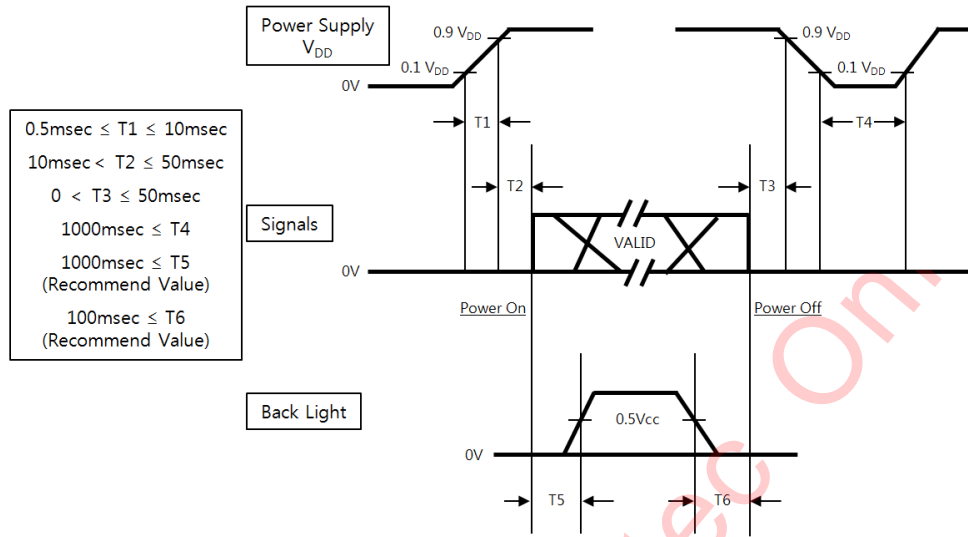
* LVDS Channel to Channel Skew(T_{chskw}) in Multiple LVDS Channels



Note : DE should be synchronized with DE per each LVDS Channel and $T_{chskw} < 16 * \text{LVDS Clock Period}$

6.4 THE SEQUENCE OF POWER ON AND OFF

To prevent the product from being latched up or the DC in the LCD module from starting an operation, the order to turn the power on and off should be changed to the order as shown in the diagram below.

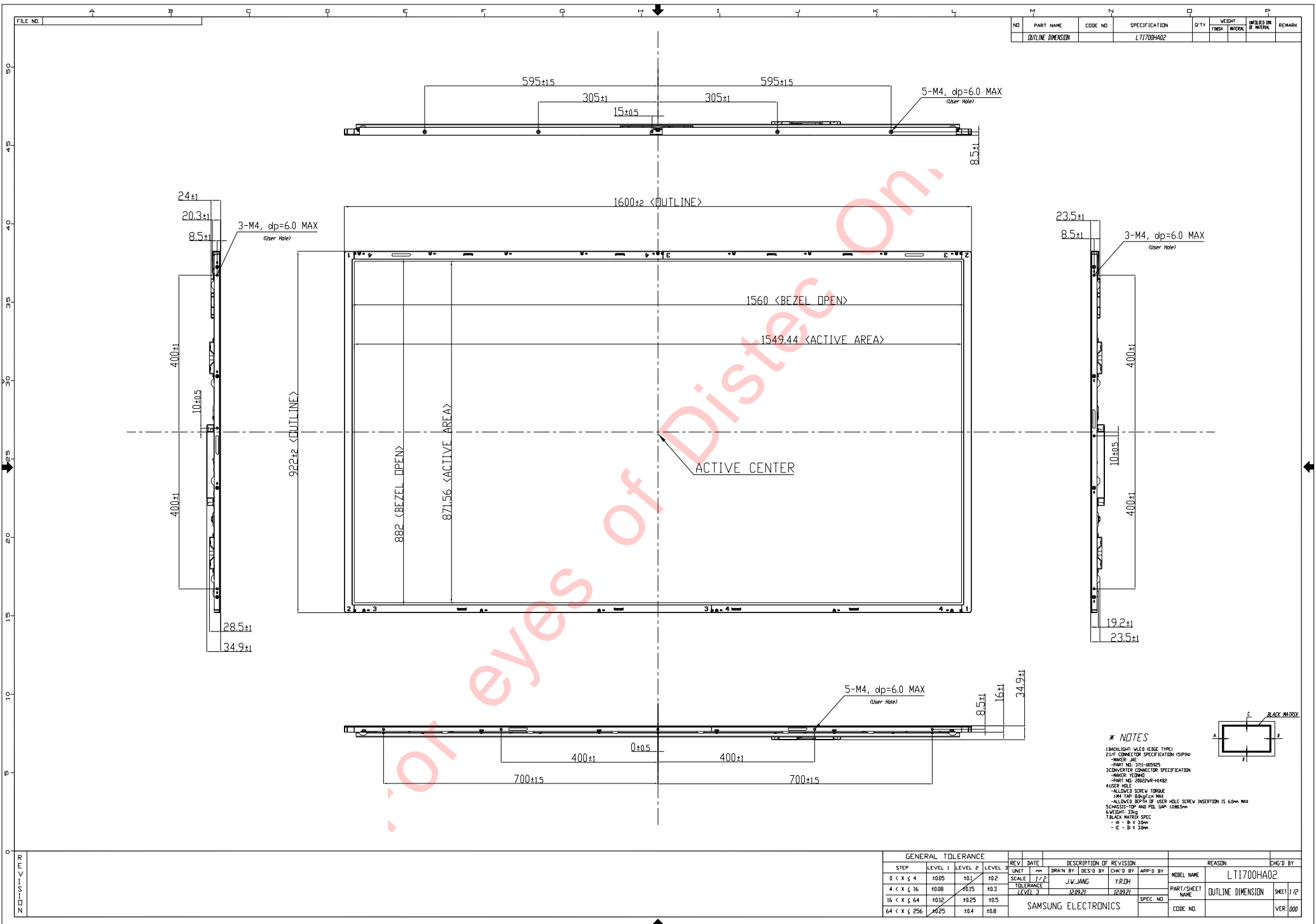


Timing	Remarks
T ₁	The time, during which the level of V _{DD} is rising from 10% to 90%.
T ₂	The changing time, during which the V _{DD} starts rising beyond 90% until the valid data of signal started coming in.
T ₃	The changing time, during which the valid data of signal starts leaving out until the V _{DD} starts falling below 90%.
T ₄	The changing time, during which the V _{DD} starts falling below 10% to restart the Windows.
T ₅	The changing time, during which the signal of BLU starts rising beyond 50%.
T ₆	The changing time, during which the signal of BLU starts falling below 50%.

- The inputted V_{DD}'s value for supply voltage, BLU, and signal to the external system of the module shall be computed with referring to the former mentioned value.
- The method to apply the voltage to the LED within the range, which the LCD operates. When the back-light is turned on before the LCD is operated or the power of LCD is turned off before the back-light is turned off, the abnormal display on the screen may be shown momentarily.
- Please keep the level of input signal low or keep the level of impedance high when the value of V_{DD} is below 10%.
- The value shall be measured after the module has been fully discharged between the period, which the power is turned on and the period, which the power is turned off like the T4 timing. The backlight may be flashed if the interface signal remains floated when the above-mentioned signal becomes invalid.

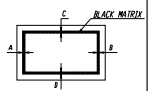
7. OUTLINE DIMENSION

Please refer next 2page.



NO	PART NAME	CODE NO	SPECIFICATION	QTY	WEIGHT TYP	WEIGHT MTRNL	WEIGHT OF PWOOD	REMARK
	OUTLINE DIMENSION		L171700HA02					

- * NOTES**
- 1) BACKLIGHT, WLED (EDGE TYPE)
 - 2) I/F CONNECTOR SPECIFICATION (SPIN)
 - MAYER, JG
 - PART NO: 3711-00920
 - 3) CONVERTER CONNECTOR SPECIFICATION
 - MAYER, YEDM0
 - PART NO: 3700-VR-H1482
 - 4) USER HOLE
 - HOLLOWED SCREW TORQUE
 - M4 TAP EDGE 0.5 MAX
 - HOLLOWED DEPTH OF USER HOLE, SCREW INSERTION IS 6.0mm MAX
 - SCHWISS- TOP AND PDL GAP 1080.5mm
 - 5) LIGHT 3mm
 - 7) BLACK MATRIX SPEC
 - A = B ± 3mm
 - C = D ± 3mm



GENERAL TOLERANCE				REV	DATE	DESCRIPTION OF REVISION			REASON	CHEK'D BY
STEP	LEVEL 1	LEVEL 2	LEVEL 3	UNIT	mm	DRAWN BY	DES'D BY	CHK'D BY	APP'D BY	MODEL NAME
0 < X < 4	±0.05	±0.1	±0.2	SCALE	1/2	J.V.JANG	Y.RDH			LTI700HA02
4 < X < 16	±0.08	±0.15	±0.3	TOLERANCE						PART/SHEET NAME
16 < X < 64	±0.12	±0.25	±0.5	LEVEL 3		12/02/21	12/02/21			OUTLINE DIMENSION
64 < X < 256	±0.25	±0.4	±0.8							SHEET 1/2
SAMSUNG ELECTRONICS										CODE NO.
										VER.000

REVISION

8. RELIABILITY TEST

Item	Test condition	Quantity
HTOL	50℃, 500hr determination	8EA
LTOL	0℃, 500hr determination	4EA
HTS	70℃, 500hr determination	4EA
LTS	-25℃, 500hr determination	4EA
THB	50℃ / 90%RH, 500hr determination	10EA
WHTS	60℃ / 75%RH, 500hr determination	4EA
T/S	-20 ~ 60℃, Dwell time : 30Min, 200cycle	4EA
TSS	-20 ~ 65℃, 220cycle	4EA
Image sticking	25℃, Mosaic pattern (9X10), 12hrs	4EA
Contact ESD	±10 kV, 210Point, 1 time/Point	3EA
Air ESD	±20 kV, 210Point, 1 time/Point	3EA
Input Con. ESD	±15kV, Input Con. Pin, 3 times/Pin	3EA
Dust	5sec spray, 5min sedimentation / 10hr Power 10min on, 10min off	2EA
Pallet Vibration → Pallet Drop	Pallet vibration : 1.05Grms, 5 ~ 200Hz, 2hr/stack side Pallet Drop : 20cm, bottom side 2 angles, 1side(Bottom)	1Pallet
Altitude	-40~50℃, 0m(0ft) ~ 13,700m(45,000ft), 72.5Hr	4EA

[Criteria on evaluation]

The components of product, which may affect to the function of display shall not be changed when the display quality test is executed under the normal operating condition.

* HTOL / LTOL : The operating at the high and low temperature*

* THB : The slant of temperature and humidity

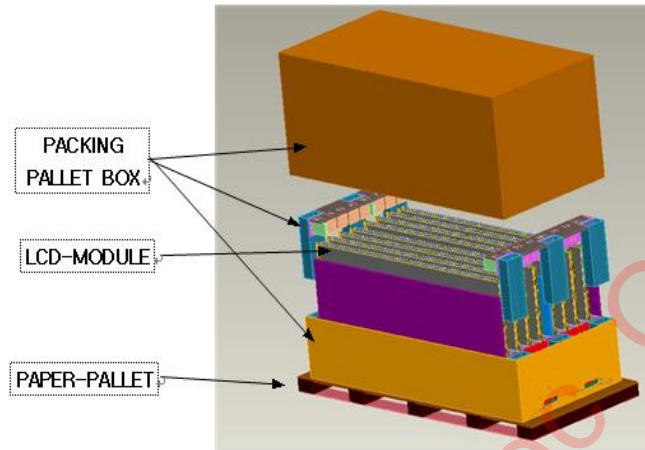
* HTS / LTS : The storage at the high and low temperature

* WHTS : The storage condition at the high temperature with the high humidity

9. PACKING

(1) Packing Form : Paper

(2) Packing Method



Note(1) Total Weight : Approximately 381.2kg

Note(2) Acceptance number of piling : 1 Pallets

Note(3) Carton size : 2025mm(H) x 1050mm(V) x 1111mm(Height)

(3) Packing Material

No	Part name	Quantity
1	Packing pallet box	1 EA
2	Shielding bag	8 EA
3	Protector panel	8 EA
4	Paper Pallet	1 EA

10. MARKINGS & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

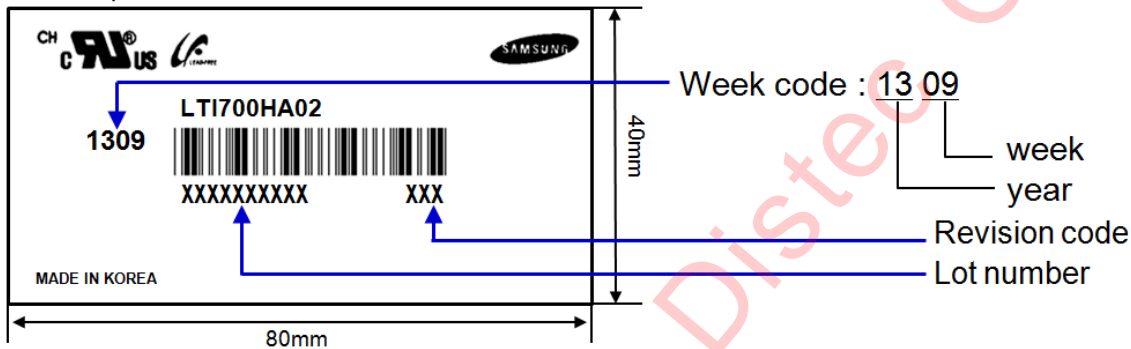
(1) Parts number : LTI700HA02

(2) Revision code : Three letters

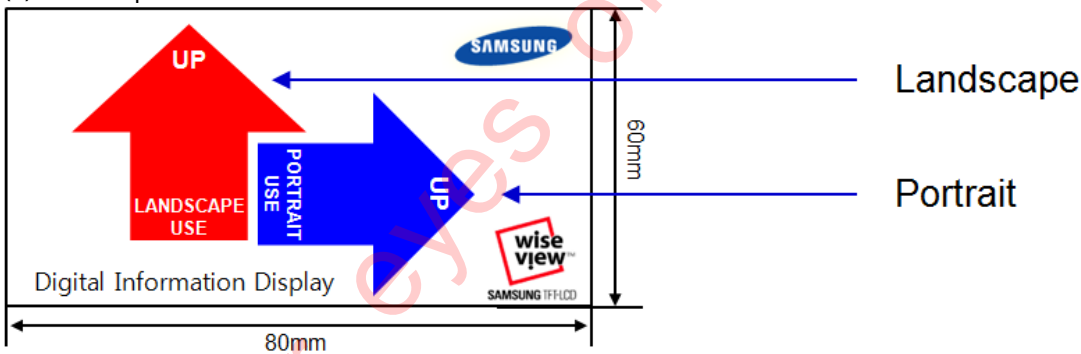
(3) Lot number : X X X X XXX XX X

- Cell Position No. (In the Glass)
- Glass No. (In the one Lot)
- Lot No. (Glass)
- Month
- Year
- Product code
- Line

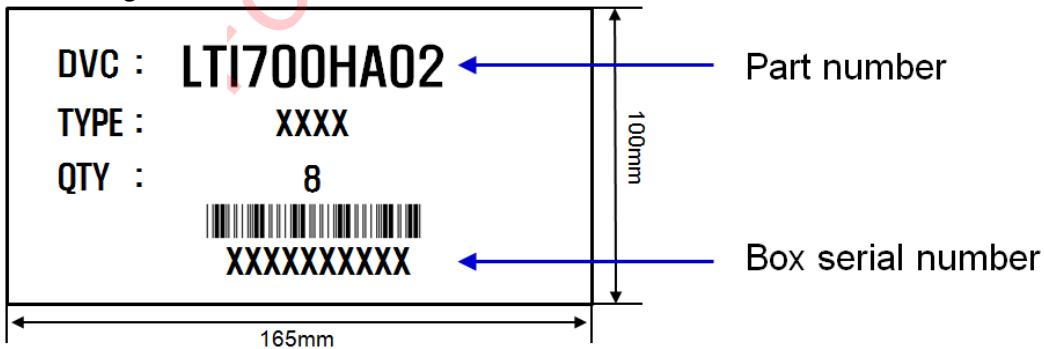
(4) Nameplate Indication



(5) Landscape / Portrait Direction Indication



(6) Packing small box attach



11. GENERAL PRECAUTIONS

11.1 HANDLING

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and LED back-light.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static. it may cause damage to the C-MOS Gate Array IC.
- (i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the LED wire.
- (l) Do not touch any component which is located on the back side.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.

11.2 STORAGE

We highly recommend to comply with the criteria in the table below.

ITEM	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	12 months		
Storage Condition	- The storage room should be equipped with a good ventilation facility, which has a temperature controlling system. - Products should be placed on the pallet, which is away from the wall not on the floor. - Prevent products from being exposed to the direct sunlight, moisture, and water. Be cautious not to pile the products up. - Avoid storing products in the environment, which other hazardous material is placed. - If products are delivered or kept in the storage facility more than 3 months, we recommend you to leave products under the condition including a 20°C temperature and a humidity of 50% for 24 hours. - If you store semi-manufactured products for more than 3 months, bake the products under the condition including the 50°C temp. and the 10% humidity for 24hrs after being used.		

11.3 OPERATION

- (a) Do not connect or disconnect the cable to/ from the module at the "Power On" condition.
- (b) The power shall be always turned on/off by the item 6.4. "Power on/off sequence"
- (c) The module has a circuit with a high frequency. The system manufacturers shall suppress the electromagnetic interference sufficiently. The methods to ground and shield are important to minimize the interference.
- (d) Design the length of cable to connect between the connector for back-light and the converter as short as possible and the shorter cable shall be connected directly.
 The longer cable between that of back-light and that of converter may cause the luminance of LED to lower and need a higher startup voltage(Vs).

11.4 OPERATION CONDITION GUIDE

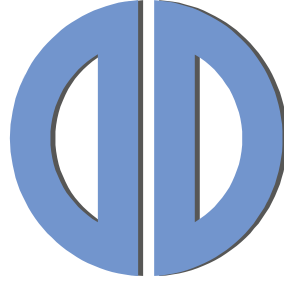
- (a) The LCD product should be operated under normal conditions.
 Normal condition is defined as below;
 - Temperature : 20±15°C
 - Humidity : 55±20%
 - Display pattern : continually changing pattern(Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SDC for Application engineering advice.
 Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

11.5 OTHERS

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Module should be turned clockwise(regular front view perspective) when used in portrait mode.
- (c) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (d) Do not exceed the absolute maximum rating value.(supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.
- (e) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (f) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (g) Please contact SDC in advance when you display the same pattern for a long time.

For eyes of Distec On

Our company network supports you worldwide with offices in Germany, Great Britain, Italy, Turkey and the USA. For more information please contact:



DATA DISPLAY GROUP

Distec GmbH

Augsburger Str. 2b
82110 Germering
Germany

Phone: +49 (0)89 / 89 43 63-0
Fax: +49 (0)89 / 89 43 63-131
E-Mail: info@datadisplay-group.de
Internet: www.datadisplay-group.de

Display Technology Ltd.

5 The Oaks Business Village
Revenge Road, Lordswood
Chatham, Kent, ME5 8LF
United Kingdom

Phone: +44 (0)1634 / 67 27 55
Fax: +44 (0)1634 / 67 27 54
E-Mail: info@datadisplay-group.co.uk
Internet: www.datadisplay-group.co.uk

Apollo Display Technologies, Corp.

87 Raynor Avenue, Unit 1 Ronkonkoma, NY
11779

United States of America

Phone: +1 631 / 580-43 60
Fax: +1 631 / 580-43 70
E-Mail: info@datadisplay-group.com
Internet: www.datadisplay-group.com

Sales Partner:

REM Italy s.a.s.

di Michieletto Flavio & C.
Via Obbia Bassa, 10
I-35010 Trebaseleghe (PD)
Italy
Phone: +39 335 521 37 89
E-Mail: info@remitaly.com
Internet: www.remitaly.com

Sales Partner:

DATA DISPLAY BİLİŞİM TEKNOLOJİLERİ İÇ VE DIŞ TİCARET LİMİTED ŞİRKETİ

Barbaros Mh Ak Zamabak Sk A Blok
D:143 Ataşehir/İstanbul
Turkey
Phone: +90 (0)216 / 688 04 68
Fax: +90 (0)216 / 688 04 69
E-Mail: info@data-display.com.tr
Internet: www.data-display.com.tr