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DISPLAY TECHNOLOGY

Datasheet

Apollo Display Technologies

G170EG01V1-V567

17" High Brightness TFT Display

UP-02-099

Content

1. Handling Precautions

1) Since front polarizer is easily damaged, pay attention not to scratch it.
2) Be sure to turn off power supply when inserting or disconnecting from input connector.
3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
7) Do not open or modify the Module Assembly.
8) Do not press the reflector sheet at the back of the module to any directions.
9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.

2. General Description

This specification applies to the 17 inch Color TFT-LCD Module G170EG01V1-V567.

The display supports the SXGA+ (1280(H) x 1024(V)) screen format and 16.7M colors (RGB 6-bits+Hi-FRC data).All input signals are 2 Channel LVDS interface compatible.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

| Items	Unit	Specifications
Screen Diagonal	[mm]	432 (17.0")
Active Area	[mm]	337.920(H) × 270.336(V)
Pixels H x V		1280 × 3(RGB) × 1024
Pixel Pitch	[mm]	0.264(per one triad) × 0.264
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Normally White
White Luminance	[cd/m²]	1000 @ max.T_{op} +55°C / 650 @ max.T_{op} +80°C
Contrast Ratio		800 : 1 (Typ)
Optical ResponseTime	[msec]	30 (Typ)
Nominal Input Voltage VDD	[Volt]	+5.0 (Typ)
Power Consumption (VDD line + LED line)	[Watt]	22 W (Typ.) (with LED driver, all black pattern)
Weight	[Grams]	2065 Typ.
Physical Size (H x V x D)	[mm]	358.5(H) x 296.5(V) Typ. x 18.0(D) Max
Electrical Interface		Dual Channel LVDS
Surface Treatment		Anti-glare type, Hardness 3H
Support Color		16.7M colors (RGB 6-bits +Hi-FRC data)
Temperature Range Operating Storage (Non-Operating)	[°C] [°C]	-30 to +80 -30 to +85
RoHS Compliance		RoHS Compliance

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C:

| Item	Unit	Conditions		Min.	Typ.	Max.	Note
Viewing Angle	[degree]	Horizontal (Right) CR = 10 (Left)		70 70	80 80	- 	1
		Vertical (Up) CR = 10 (Down)		50 70	60 80	- 	
Luminance Uniformity	[%]	9 Points		75	80	-	2, 3
Optical Response Time	[msec]	Rising		-	20	25	4, 6
		Falling		-	10	15	
		Rising + Falling		-	30	40	
Color / Chromaticity Coordinates (CIE 1931)		Red x		0.598	0.648	0.698	4
		Red y		0.289	0.339	0.389	
		Green x		0.273	0.323	0.373	
		Green y		0.563	0.613	0.663	
		Blue x		0.093	0.143	0.193	
		Blue y		0.020	0.070	0.120	
		White x		0.263	0.313	0.363	
		White y		0.279	0.329	0.379	
White Luminance	[cd/m²]	max. T_{op} = +80°C max. T_{op} = +55°C		-	650 1000	-	4
Contrast Ratio				600	800	-	4
Cross Talk (At 75Hz)	[%]			-	-	1.5	5
Flicker	[dB]			-	-	-20	7
Color Gamut	[%]				72		

Optical Equipment: BM-5A, BM-7, PR880, or equivalent

Note 1: Definition of viewing angle

180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.
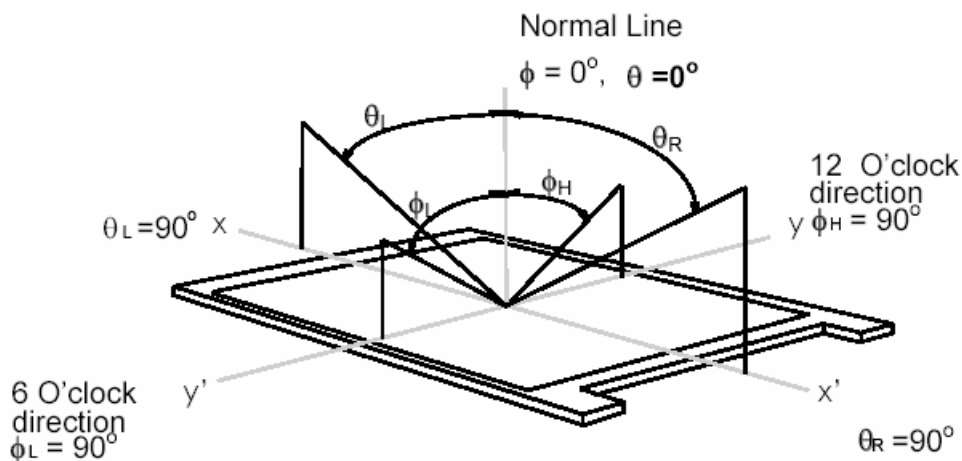

Note 2: 9 points position
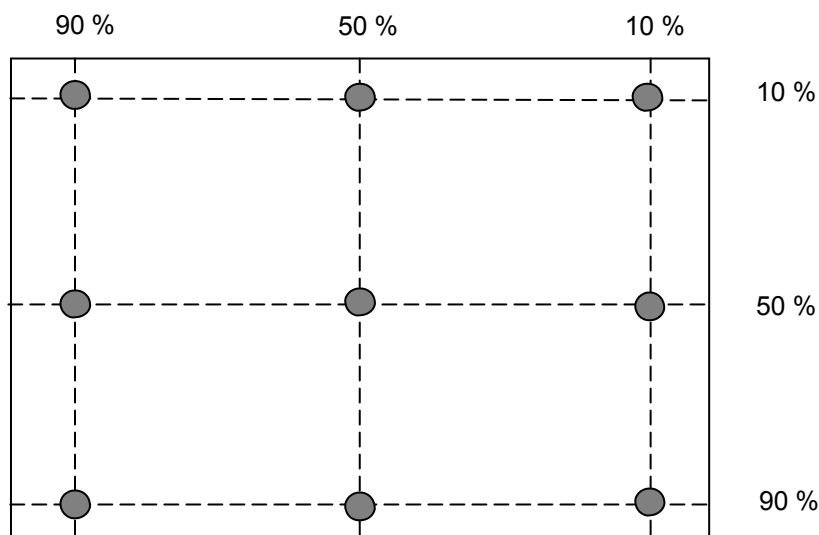
Note 3: The luminance uniformity of 9 points is defined by dividing the maximum luminance values by the minimum test point luminance

$$\delta_{W9} = \frac{\text{Minimum Luminance of 9 points}}{\text{Maximum Luminance of 9 points}}$$

Note 4: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.
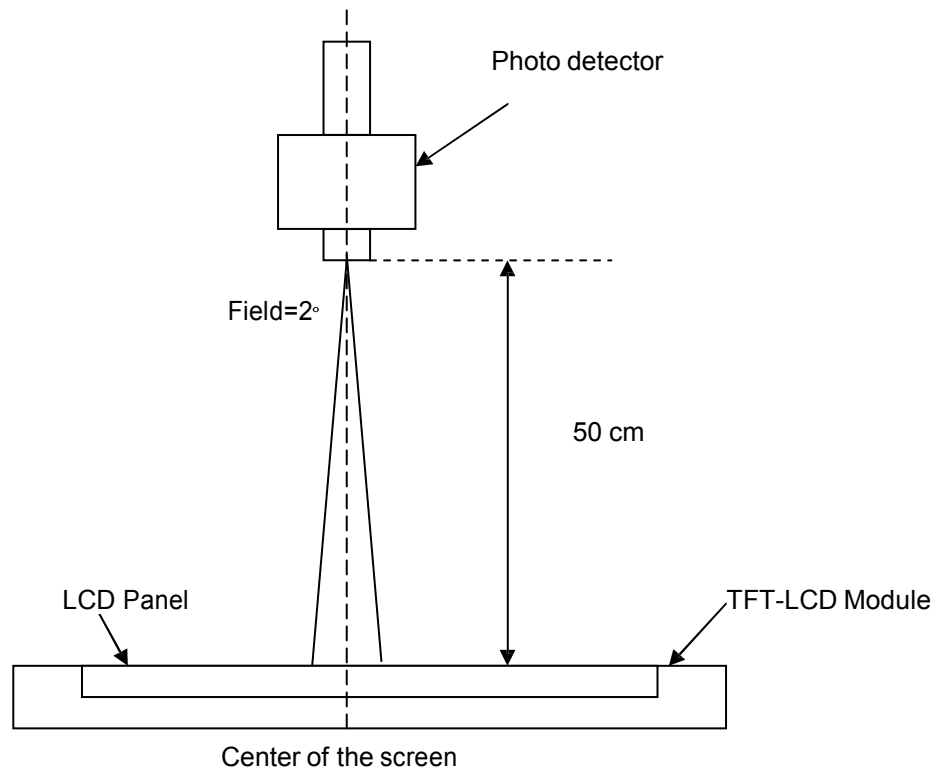
Note 5: Definition of Cross Talk (CT)

CT = | YB – YA | / YA × 100 (%)

Where

    YA = Luminance of measured location without gray level 0 pattern (cd/m2)

    YB = Luminance of measured location with gray level 0 pattern (cd/m2)
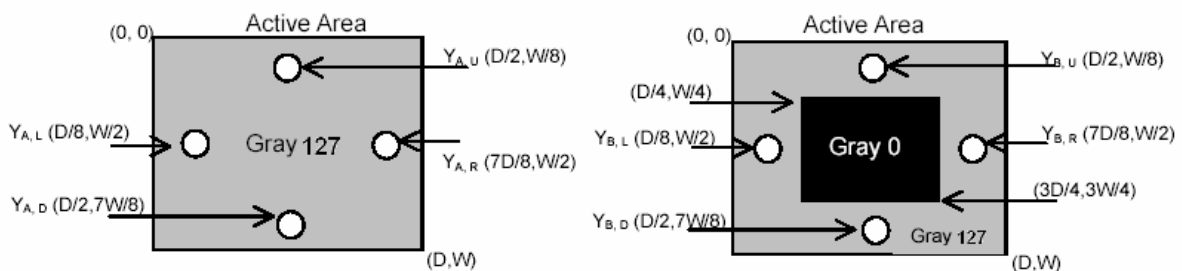
Note 6: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time), and from "Full White" to "Full Black "(falling time), respectively. The response time is interval between the 10% and 90% of amplitudes. Please refer to the figure as below.
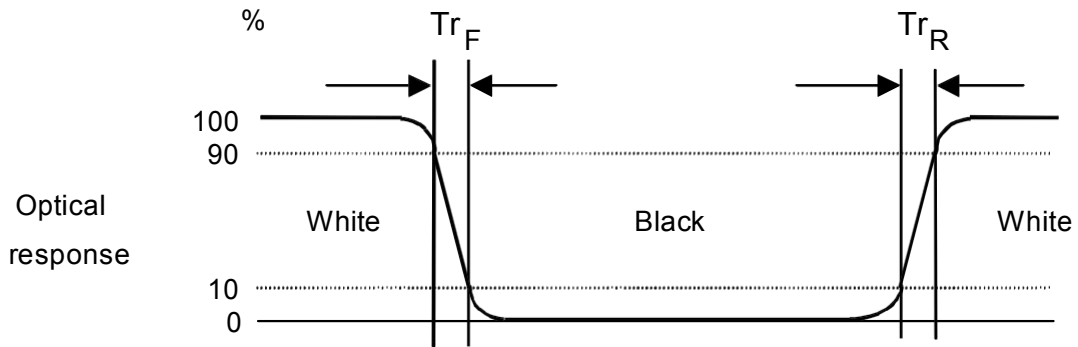
Note 7: Subchecker Pattern
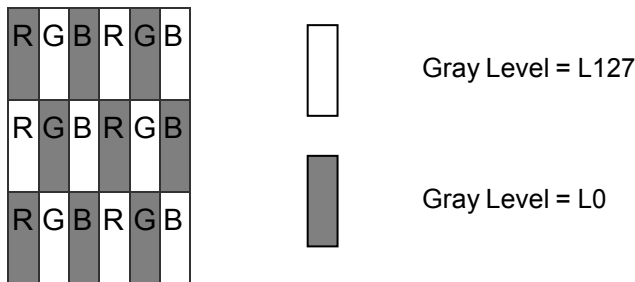
Method: Record dBV & DC value with (WESTAR)TRD-100
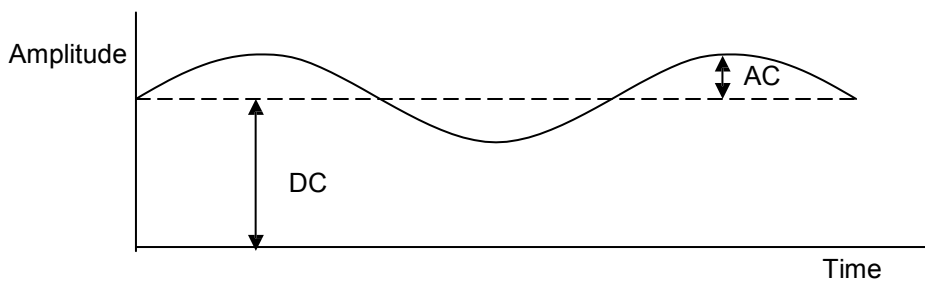
$$\text{Flicker (dB)} \cong 20\log\frac{AC\ \text{Level(at 30 Hz)}}{DC\ \text{Level}}$$

3. Functional Block Diagram

The following diagram shows the functional block of the 17.0 inches Color TFT-LCD Module:
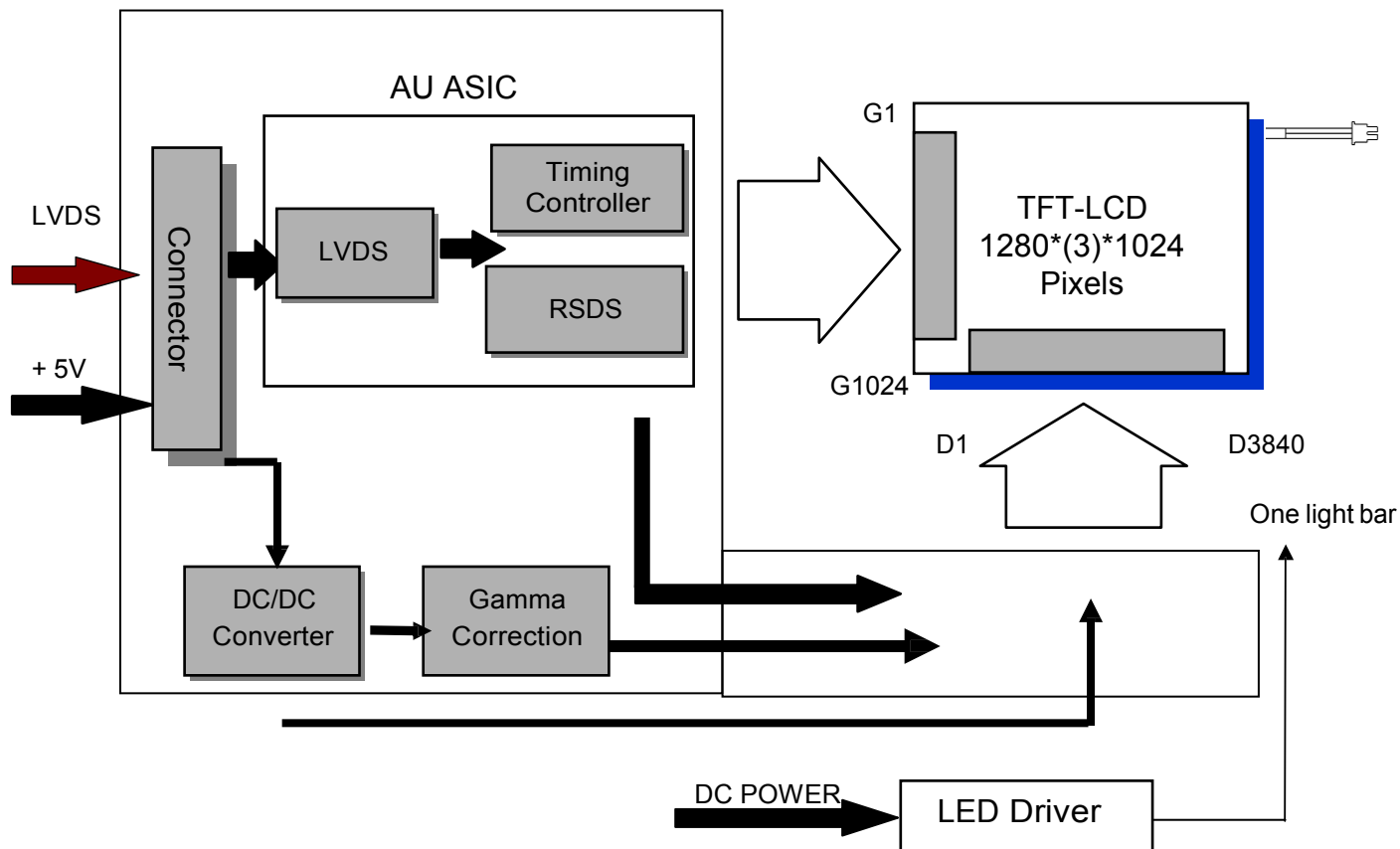
PCBA Connector:

JAE FI-XB30SSL-HF15
Or Compatible

LED Driver Connector:

Molex 22-01-3027
Or Compatible

4. Absolute Maximum Ratings

Absolute maximum ratings of the module is as following:

4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VIN	-0.3	6	[Volt]	Note 1,2

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit	Conditions
Operating Temperature	TOP	-30	+80	[°C]	
Operation Humidity	HOP	8	90	[%RH]	Note 3
Storage Temperature	TST	-30	+85	[°C]	
Storage Humidity	HST	8	90	[%RH]	

Note 1: With in Ta (25   )

Note 2: Permanent damage to the device may occur if exceed maximum values

Note 3: For quality performance, please refer to AUO IIS (Incoming Inspection Standard).
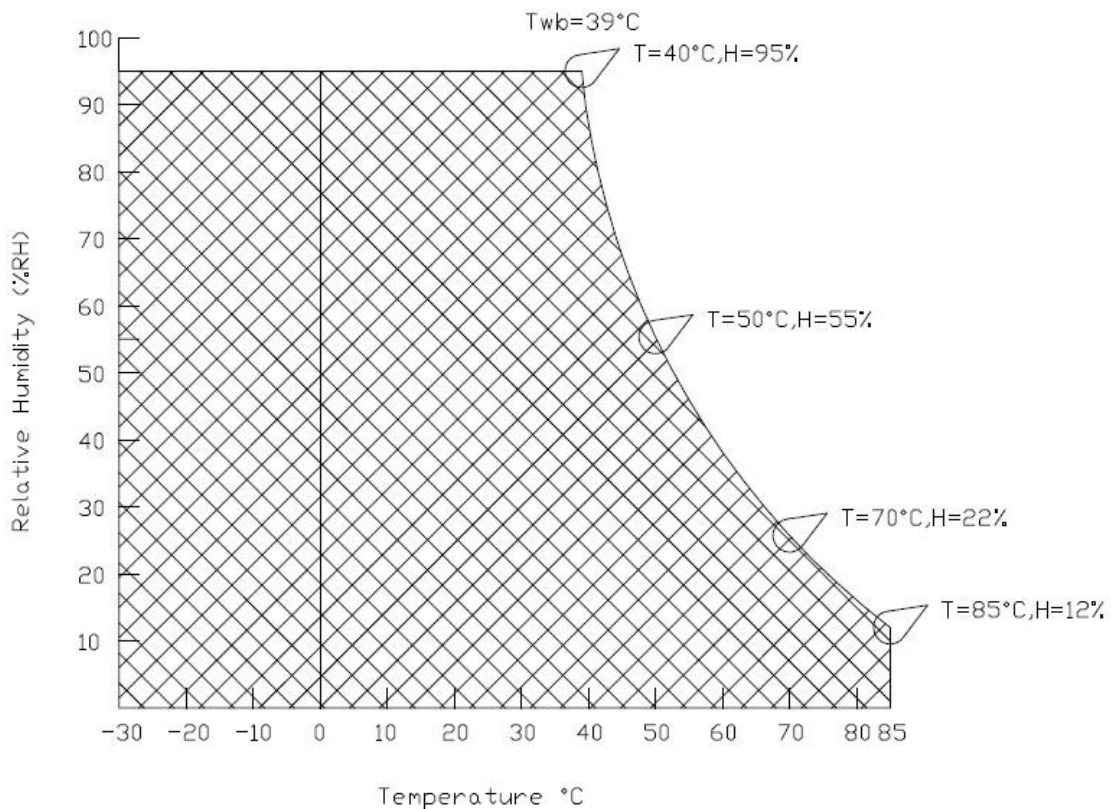
5. Electrical characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

Input power specifications are as follows:

Symble	Parameter	Min.	Typ.	Max.	Unit	Condition
VCC	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	±10%
ICC	Input Current	-	1.05	1.16	[A]	Vin=5V , All Black Pattern, at 75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note
PCC	VCCPower	-	5.25	5.8	[Watt]	Vin=5V , All Black Pattern, at 75Hz
VCCrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	200	[mV] p-p	With panel loading

Note: Measurement conditions:
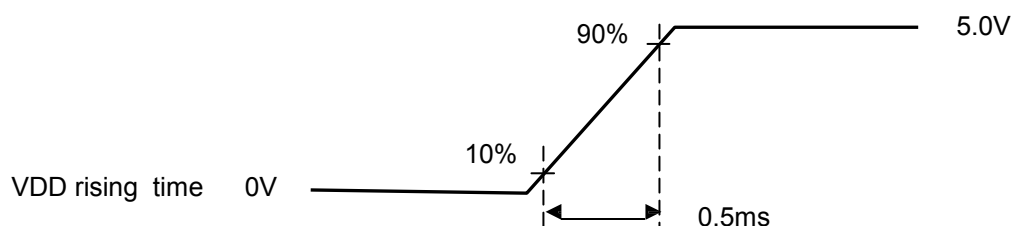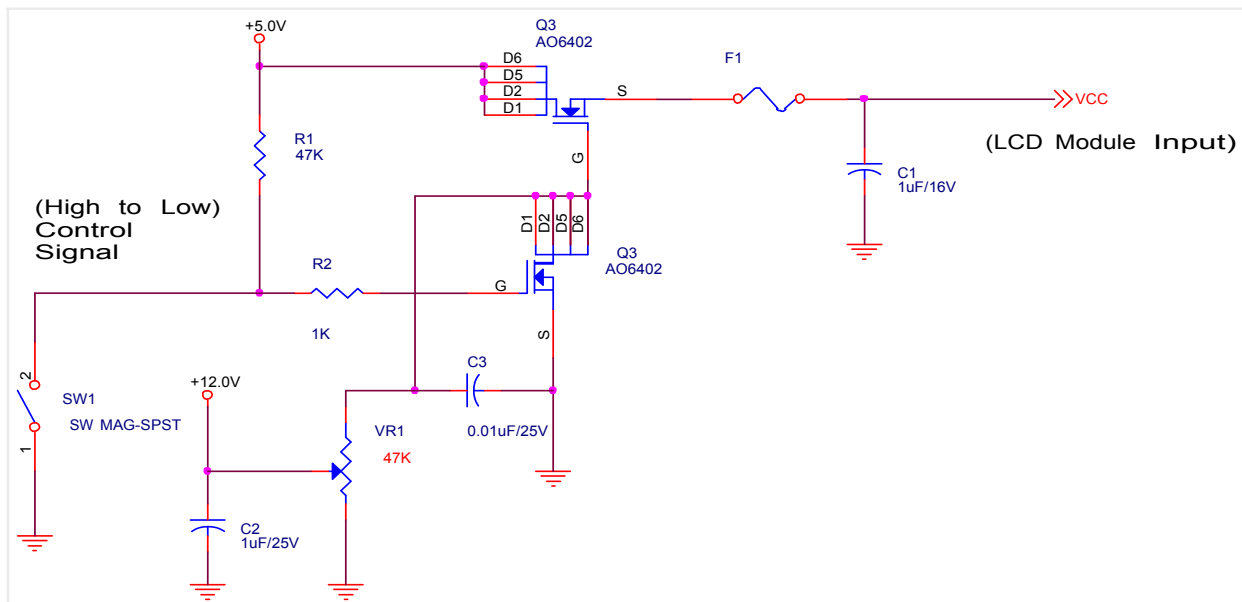
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when Vin is off
It is recommended to refer the specifications of SN75LVDS82DGG (Texas Instruments) in detail.

Each signal characteristics are as follows;

Symbol	Parameter	Min	Typ	Max	Units	Condition
VTH	Differential Input High Threshold	-	-	+100	[mV]	VICM = 1.2V Note
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VICM = 1.2V Note
│VID│	Input Differential Voltage	100	400	600	[mV]	Note
VICM	Differential Input Common Mode Voltage	+1.0	+1.2	+1.5	[V]	VTH/VTL = ±100MV Note

Note: LVDS Signal Waveform
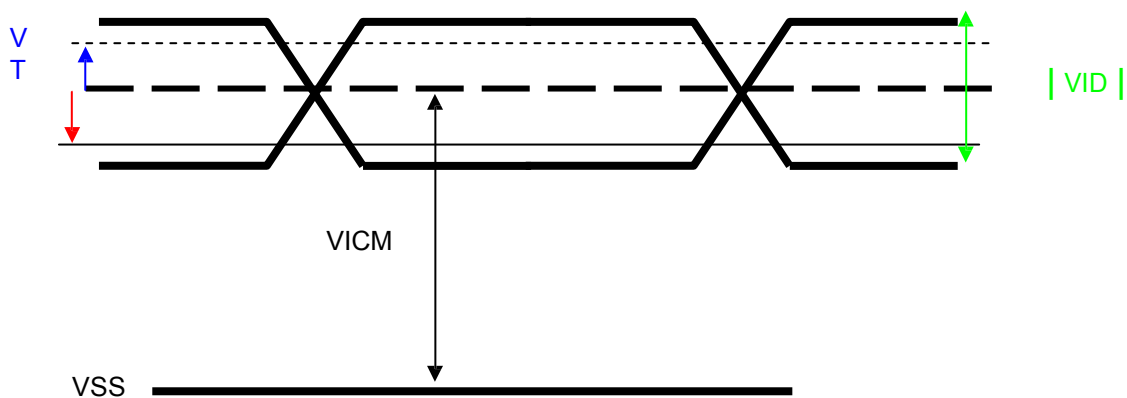
5.2 Backlight Unit

Apollo LED Backlight Unit (BLU)	147000-0	$T_a = 25 \pm 2^oC$				
Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
BLU Input Voltage	V_f	7.4	9.24	-	V_{DC}	4,6
BLU Forward Current	I_f	-	-	1300	mA_{DC}	2,3,5
BLU Power Consumption	P_{LED}	-	-	12	W	1,2
BLU Lifetime	L_L	-	70,000	-	Hrs	1

Notes:

(1) The lifetime of the LED BLU is defined as the number of hours it continues to operate at $T_a = 25 \pm 2^oC$ and $I_f = 1300mA_{DC}$ until the luminance is 50% of its original value. Operating the BLU at $T_a > 25 \pm 2^oC$ or applying power > 12W will reduce the lifetime and cause color shifting.

(2) Specified values are for a single LED BLU connector, this module contains two BLU connectors.

(3) Specified current equals input to a single LED BLU connector at 100% duty cycle.

(4) Specified voltage is input to LED BLU at typical and maximum current values & 100% duty cycle.

(5) It is recommended to dim LED BLU using PWM and to synchronize PWM with frame frequency.

(6) $V_f = \Delta_{BLU1}^{BLUn}(V_A - V_C)$

Recommended backlight power supply

PART NUMBER	MANUFACTURER
SmartLED-II 2x1.3A IN-52-005 – analog IN-52-010 - PWM	Distec GmbH

6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.
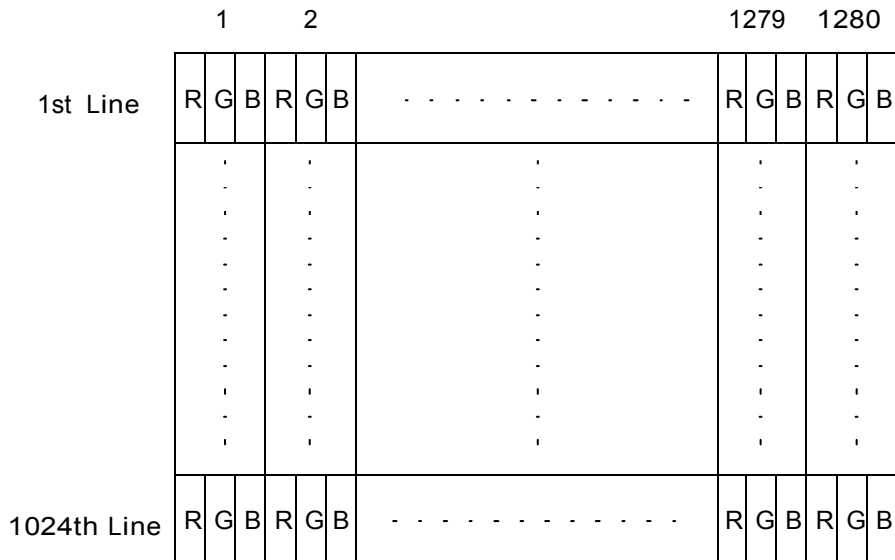
6.2 The Input Data Format
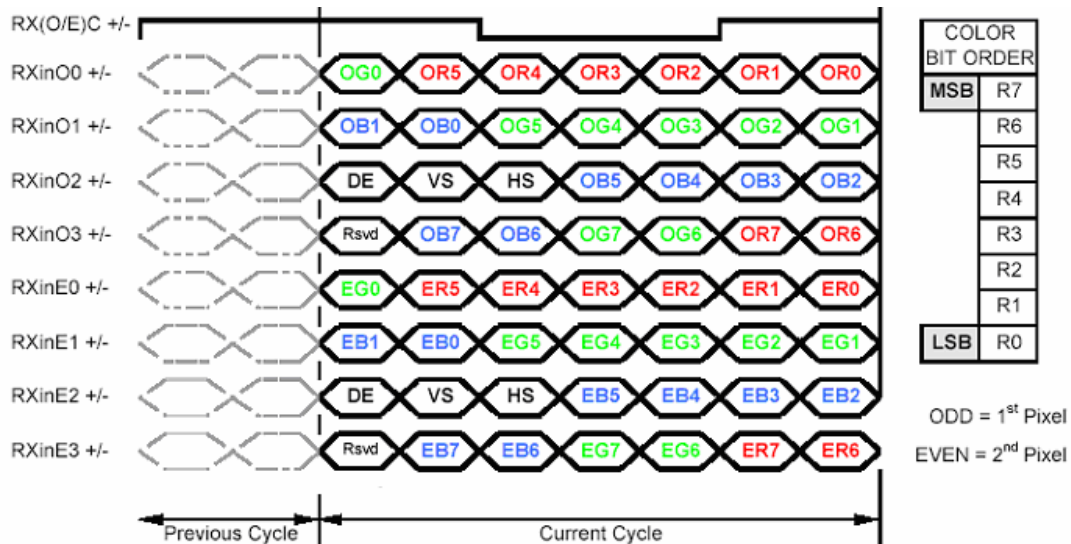
Note1: Normally, DE, VS, HS on EVEN channel are not used.
Note2: Please follow PSWG.
Note3: 8-bit in

6.3 Signal Description

The module using a pair of LVDS receiver SN75LVDS82(Texas Instruments) or compatible. LVDS is a differential signal technology for LCD interface and high speed data transfer device. Transmitter shall be SN75LVDS83(negative edge sampling) or compatible. The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels.

| PIN #	SIGNAL NAME	DESCRIPTION
1	RxO0-	Negative LVDS differential data input (Odd data)
2	RxO0+	Positive LVDS differential data input (Odd data)
3	RxO1-	Negative LVDS differential data input (Odd data)
4	RxO1+	Positive LVDS differential data input (Odd data)
5	RxO2-	Negative LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
6	RxO2+	Positive LVDS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)
7	GND	Power Ground
8	RxOC-	Negative LVDS differential clock input (Odd clock)
9	RxOC+	Positive LVDS differential clock input (Odd clock)
10	RxO3-	Negative LVDS differential data input (Odd data)
11	RxO3+	Positive LVDS differential data input (Odd data)
12	RxE0-	Negative LVDS differential data input (Even data)
13	RxE0+	Positive LVDS differential data input (Even data)
14	GND	Power Ground
15	RxE1-	Negative LVDS differential data input (Even data)
16	RxE1+	Positive LVDS differential data input (Even data)
17	GND	Power Ground
18	RxE2-	Negative LVDS differential data input (Even data)
19	RxE2+	Positive LVDS differential data input (Even data)
20	RxEC-	Negative LVDS differential clock input (Even clock)
21	RxEC+	Positive LVDS differential clock input (Even clock)
22	RxE3-	Negative LVDS differential data input (Even data)
23	RxE3+	Positive LVDS differential data input (Even data)
24	GND	Power Ground
25	GND	Power Ground (For AUO test Aging+HVS mode)
26	NC	No contact
27	GND	Power Ground
28	VCC	+5.0V Power Supply
29	VCC	+5.0V Power Supply
30	VCC	+5.0V Power Supply

Note1: Start from left side

Connector

1   30

RxO0-   VCC
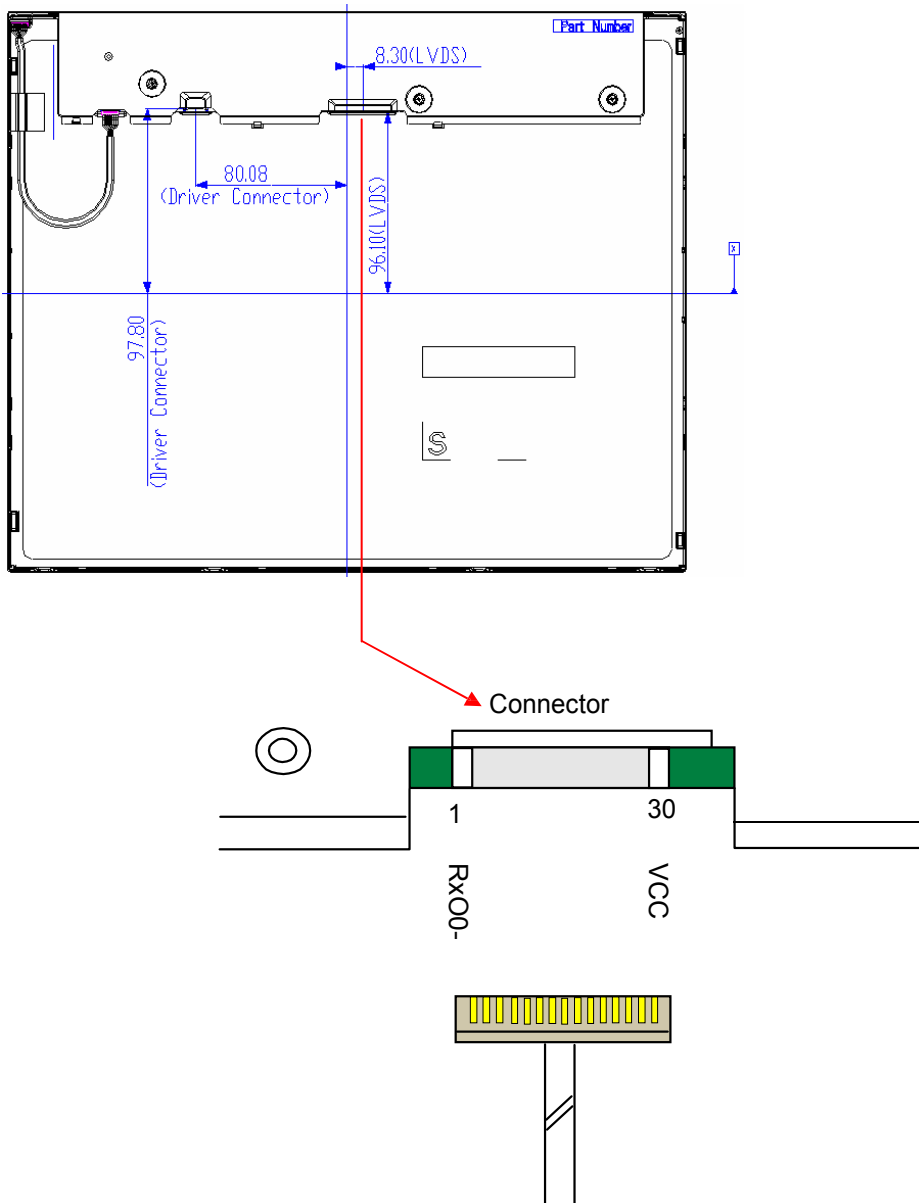
Note2: Input signals of odd and even clock shall be the same timing.

Note3: Please follow PSWG.

6.4 Timing Characteristics

6.4.1 Timing Characteristics

Basically, interface timings described here is not actual input timing of LCD module but output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

Signal	Item	Symbol	Min	Typ	Max	Unit
Vertical Section	Period	Tv	1034	1066	2048	Th
	Active	Tdisp(v)	1024	1024	1024	Th
	Blanking	Tbp(v)+Tfp(v)+PWvs	10	42	1024	Th
Horizontal Section	Period	Th	750	844	2048	Tclk
	Active	Tdisp(h)	640	640	640	Tclk
	Blanking	Tbp(h)+Tfp(h)+PWhs	110	204	1408	Tclk
Clock	Period	Tclk	14.81	18.52	25	ns
	Frequency	Freq	40	54	70	MHz
Frame rate	Frame rate	F	49	60	76	Hz

Note : DE mode only

6.4.2 Timing Diagram
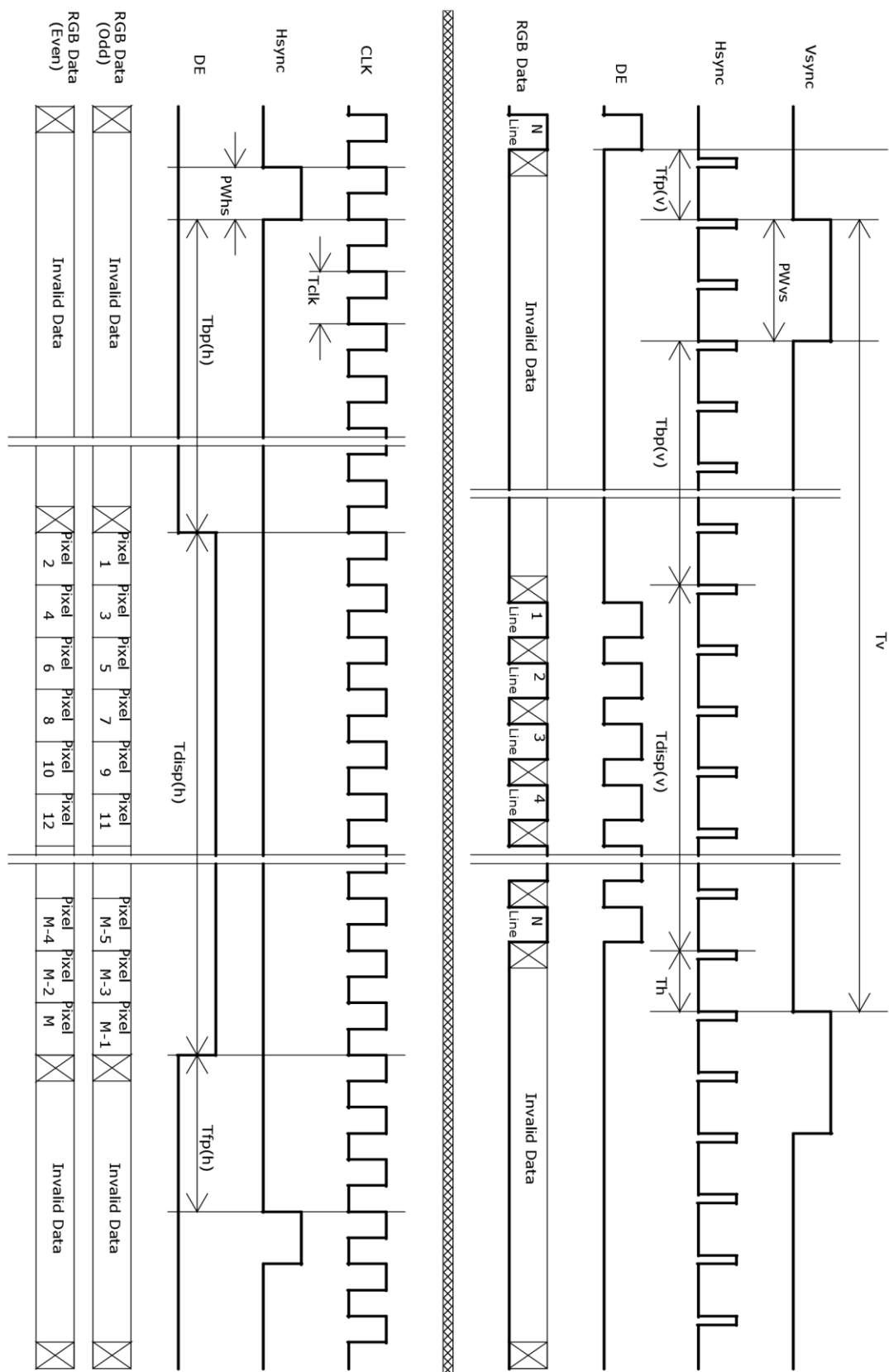
6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.

Power Sequence Timing
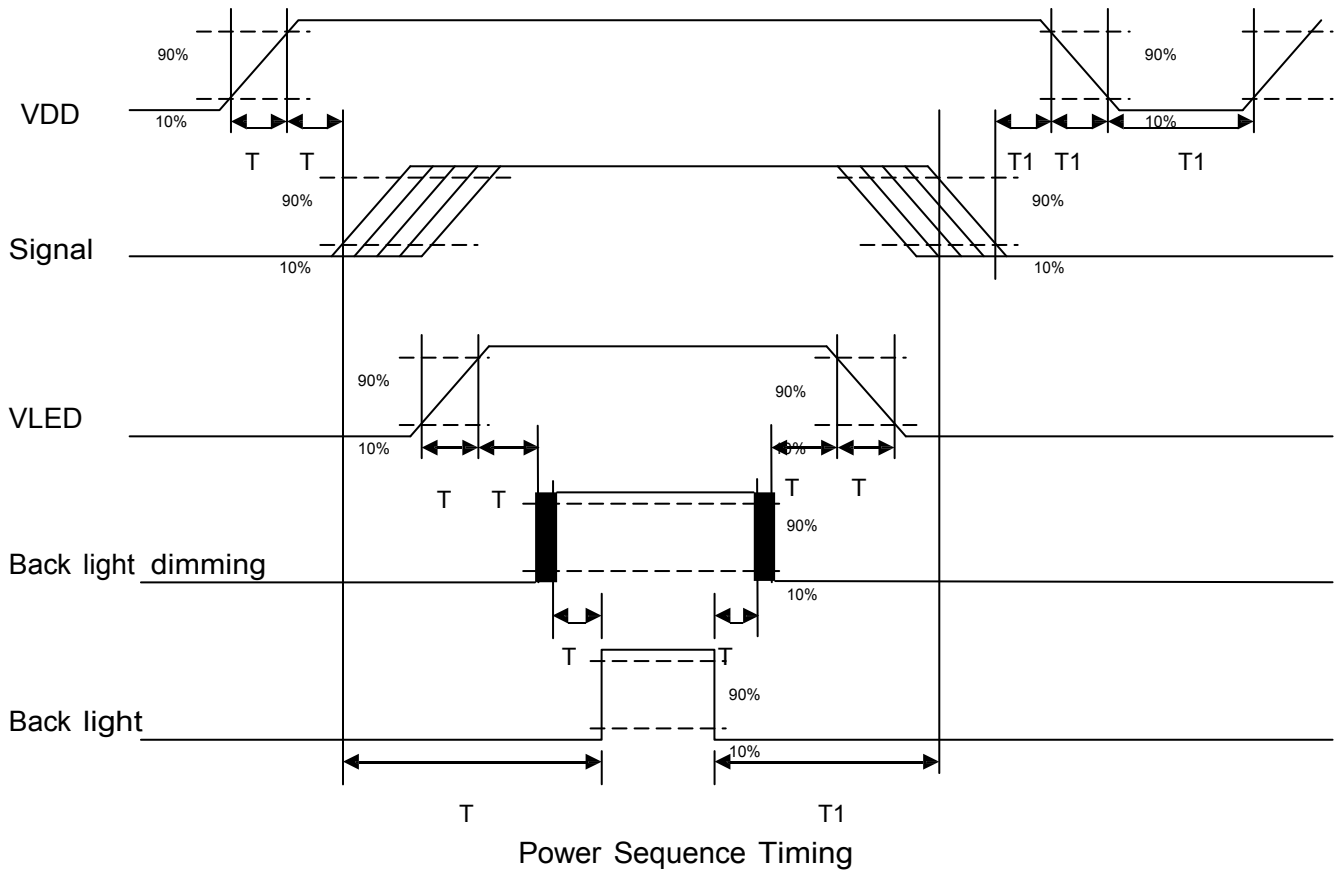
Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	30	40	50	[ms]
T3	200	-	-	[ms]
T4	0.5	-	10	[ms]
T5	10	-	-	[ms]
T6	10	-	-	[ms]
T7	0	-	-	[ms]
T8	10	-	-	[ms]
T9	-	-	10	[ms]
T10	110	-	-	[ms]
T11	0	16	50	[ms]
T12	-	-	10	[ms]
T13	1000	-	-	[ms]

7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

7.1.1 Connector

Connector Name / Designation	Interface Connector
Manufacturer	JAE or Compatible
Type Part Number	FI-XB30SSL-HF15 or Compatible
Mating Housing Part Number	JAE FI-X30HL or Compatible

7.1.2 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	RxOIN0-	2	RxOIN0+
3	RxOIN1-	4	RxOIN1+
5	RxOIN2-	6	RxOIN2+
7	GND	8	RxOCLKIN-
9	RxOCLKIN+	10	RxOIN3-
11	RxOIN3+	12	RxEIN0-
13	RxEIN0+	14	GND
15	RxEIN1-	16	RxEIN1+
17	GND	18	RxEIN2-
19	RxEIN2+	20	RxECLKIN-
21	RxECLKIN+	22	RxEIN3-
23	RxEIN3+	24	GND
25	GND (AGMODE+HVS)	26	NC
27	GND	28	VCC
29	VCC	30	VCC

7.2 Backlight Unit

Connector Name	LED BLU Connector
Connector Quantity	2
Connector Manufacturer | P/N	Molex | 22-01-3027
Connector Mate	Molex | 22-05-3021
Connector Length	12"

LED BLU CONN PIN ASSIGNMENT			
Pin	**Name**	**Description**	**Color**
1	LED+	Anode	Red
2	LED-	Cathode	Black

8. Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 5 , 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 80 , 300hours	
Low Temperature Operation (LTO)	Ta= -3	
High Temperature Storage (HTS)	Ta= 85	
Low Temperature Storage (LTS)	Ta= -3	
Vibration Test (Non-operation)	Acceleration: 1.5 G Wave: Random Frequency: 10 - 200 - 10 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 60 cm, package test	
Thermal Shock Test (TST)	- 30min, 6 30min, 100 cycles	
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	1
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	
EMI	30-230 MHz, limit 40 dBu V/m, 230-1000 MHz, limit 47 dBu V/m	
Altitude Test	Operation:10,000 ft Non-Operation:30,000 ft	

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost
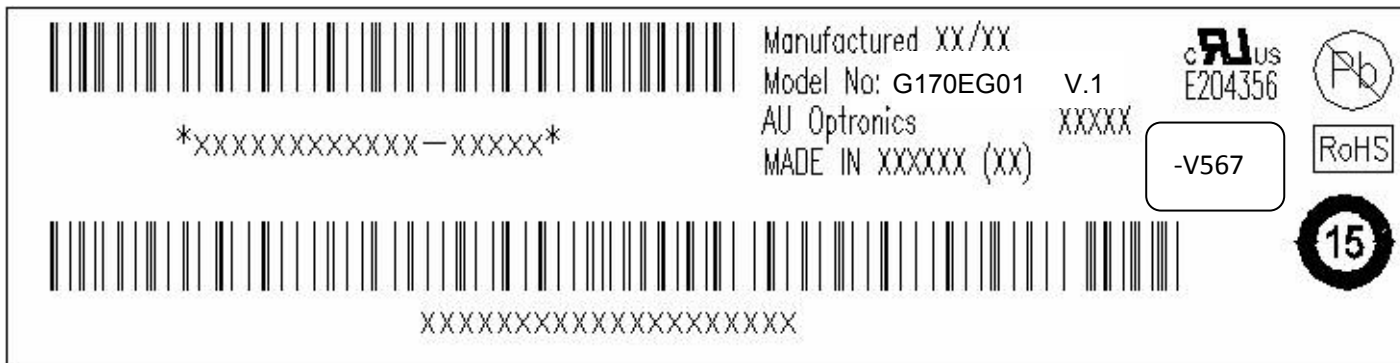
Self-recoverable. No hardware failures.

Note2:

- Water condensation is not allowed for each test items.

- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.

- The reliability test is performed only to examine the TFT-LCD module capability.

- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.

- No function failure occurs.

9. Label and Packaging

9.1 Shipping Label (on the rear side of TFT-LCD display)
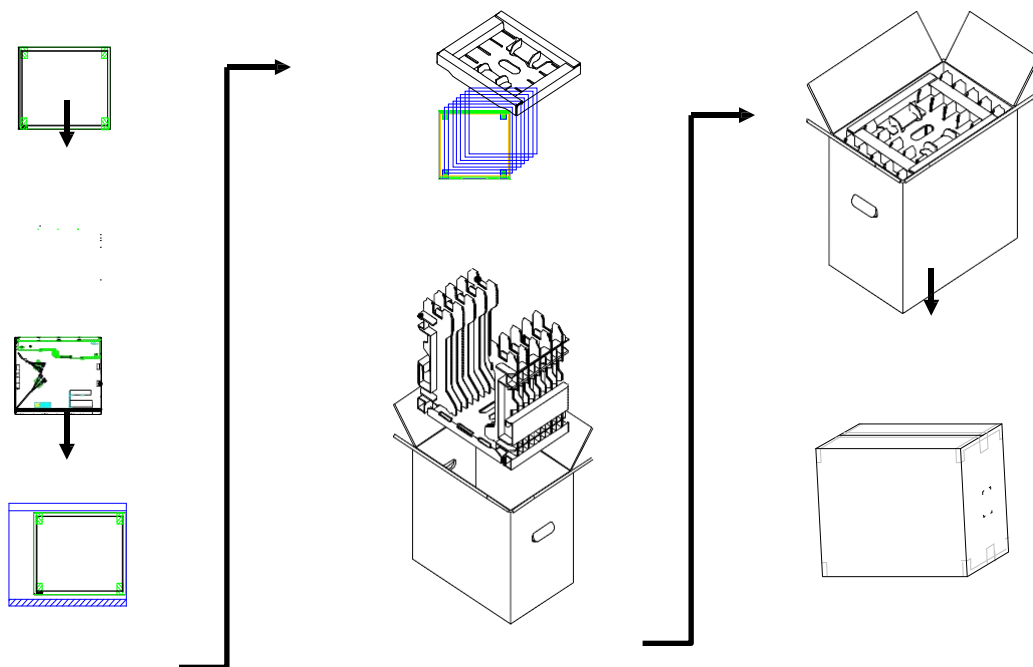
The shipping label format is shown as below.


```
Manufactured XX/XX
Model No: G170EG01    V.1
AU Optronics          XXXXX
MADE IN XXXXXX (XX)          -V567
```

XXXXXXXXXXXX—XXXXX

XXXXXXXXXXXXXXXXXXXX

9.2 Carton Package

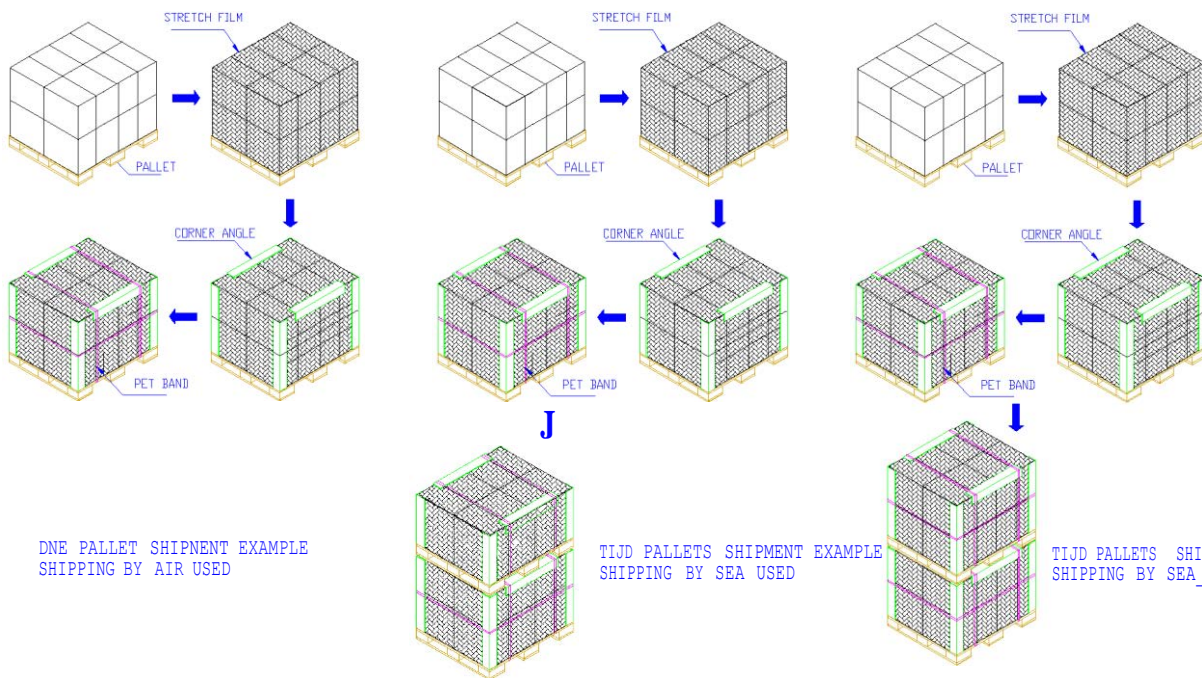Max capacity：8 TFT-LCD module per carton
Max weight: 18 kg per carton
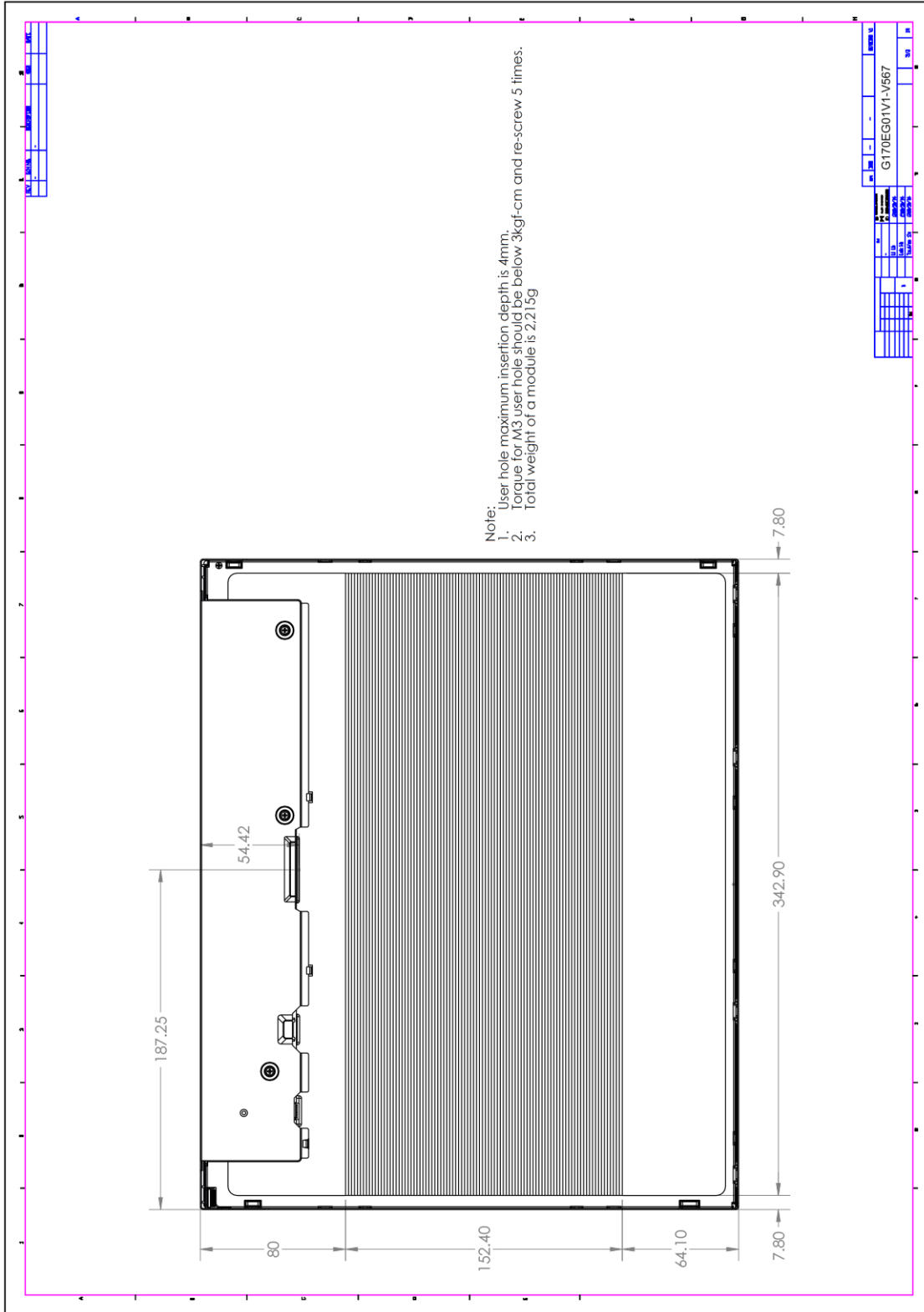Outside dimension of carton:434(L)mm*278(W)mm*391(H)mm

9.3 Palletizing

	MAX_SHIPPING BY AIR	MAX_SHIPPING BY SEA	MAX_SHIPPING BY SEA_HQ	
MODULE/CARTON	8	8	7	
CARTON/LAYER			8	
LAYER/PALLET		3+1	3+2	
MODULE/PALLET	192	256	280	
CARTON SIZE(MM)	434CL)*278CIJ)*391CHl	434CL)*278Cwl*391CHl	434CU*27BCwl*391CHl	
PALLET SIZE(MM)	1140(L)*890(W)*135(H)	1140CL)*890(wl*135CHl	1140CU*B9OCIJl*135CHl	
TOTAL PALLET HEIGHT(MM)	1308	1834	2258	
TOTAL PALLET WEIGHT(KG)	445	602	746	

ONE PALLET SHIPMENT EXAMPLE SHIPPING BY AIR USED

TIJD PALLETS SHIPMENT EXAMPLE SHIPPING BY SEA USED

TIJD PALLETS SHIPMENT EXAMPLE SHIPPING BY SEA_HQ USED

10. Mechanical Characteristics

10.1 Outline Dimensions (Front View)

10.1 Outline Dimensions (Rear View)

Note:
1. User hole maximum insertion depth is 4mm.
2. Torque for M3 user hole should be below 3kgf-cm and re-screw 5 times.
3. Total weight of a module is 2,215g

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